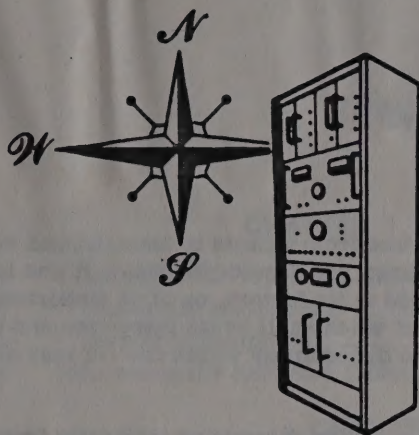




**WILCOX**

AN AMERICAN-STANDARD COMPANY

14TH AND CHESTNUT

KANSAS CITY, MISSOURI 64127

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# INSTRUCTION MANUAL

**MANUAL No. 104984-300**

R-1250/GR

**MODEL 792**

**VHF RECEIVER**

**PART No. 097870-0100/0200/0300**

**June 15, 1970**

**Serial No. \_\_\_\_\_**

VHF RECEIVER 792

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## INTRODUCTION

This publication is the instruction manual for VHF Receiver Model 792A, a ground-based radio receiving unit which operates on a single preset channel within the frequency range from 108.00 to 149.95 megacycles. The receiver is used at air traffic control facilities for the reception of amplitude-modulated radiotelephone signals. The receiver is designed for unattended operation and may be either used as an independent unit or operated as part of a vhf communications system employing

50-kilocycle channel spacing. The manual consists of seven chapters bound in one volume. Chapter 1 contains general information and leading particulars; the installation phase is covered in Chapter 2; complete operating instructions are found in Chapter 3; principles of operation are described in Chapter 4; Chapter 5 provides instructions for maintaining and overhauling the receiver; the circuit diagrams are contained in Chapter 6; and a parts list will be found in Chapter 7.



120-038-P

*Figure 1-1. VHF Receiver Model 792.*

## CHAPTER 1

### GENERAL INFORMATION

#### 1-1. DESCRIPTION AND PURPOSE

#### 1-2. DESCRIPTION OF EQUIPMENT.

1-3. VHF Receiver Model 792 is a ground-based, radio receiving unit that operates on a single preset channel within the range of 116.00 to 149.95 mc for part No. 097870-0100/-0300 or 108.00 to 118.00 mc for part No. 097870-0200. The receiver employs a single-conversion superheterodyne circuit with a crystal-controlled oscillator and is a complete, operable unit less antenna, primary power source, remote control equipment, and loudspeaker or headset. Remote control and loudspeaker requirements for the receiver are compatible with FSA-22 and FAA4 remote control equipment.

1-4. The receiver, shown in figure 1-1, is designed for panel mounting in a standard 19-inch equipment rack. Receiver parts are located on a horizontal chassis behind the front panel, and protection is provided at the top and bottom of the chassis by removable equipment covers. All controls, switches, and indicators necessary for operation of the receiver are mounted on the front panel. The controls and switches permit local adjustment of the audio gain, local or remote adjustment of the rf gain/squelch threshold, and local or remote switching to activate or disable the squelch circuit. A headset jack is included on the front panel to permit monitoring of the receiver audio output.

1-5. Mounted on the rear of the chassis is a carrier operated relay that performs both internal and external control functions. All connections to the receiver are made through a 19-pin connector, an ac power cord, and a coaxial antenna connector on the rear of the chassis. Primary power is normally obtained from a 115-volt, 50- to 400-cps, ac source, but facilities are provided in the receiver for operation from a 230-volt, 50- to 400-cps, ac source or a 27.5-volt battery or dc generator. The receiver also contains a charging circuit to permit trickle recharging of an external battery from either a 115- or 230-volt ac source.

#### 1-6. PURPOSE OF EQUIPMENT.

1-7. VHF Receiver Model 792 is used as a ground based receiver at air traffic control facilities for the reception of amplitude-modulated radiotelephone signals. The receiver is designed for unattended operation and may be either used as an independent unit or operated as part of a vhf communications system employing 50-kilocycle (kc) channel spacing.

#### 1-8. INFORMATION AND REFERENCE TABLES.

1-9. Tables 1-1 through 1-4 contain information that will help you to become familiar with all the features and characteristics of the receiver.

*Table 1-1. Leading Particulars*

|                           |                                                                                                                                         |
|---------------------------|-----------------------------------------------------------------------------------------------------------------------------------------|
| PRIMARY POWER             | 115 or 230 volts ac, $\pm 10$ percent; 50-400 cps; single phase;<br>27 watts max<br>or<br>27.5 volts dc, $\pm 10$ percent; 14 watts max |
| TRANSPORTABILITY          | Light vehicles                                                                                                                          |
| PHYSICAL CHARACTERISTICS: |                                                                                                                                         |
| Dimensions (Inches)       | 1.72 x 9.50 x 19.00                                                                                                                     |
| Weight                    | 6.4 lb                                                                                                                                  |
| VIBRATION:                |                                                                                                                                         |
| Frequency                 | 15 to 55 cps with a 1.0 to 1.5 cycle-per-second-per-second rate<br>of change                                                            |
| Amplitude                 | 0.015 inch total excursion                                                                                                              |
| Duration                  | 45 minutes along each of three mutually perpendicular planes                                                                            |
| BOUNCE (Crated)           | Average maximum recurring acceleration of 10 g for a dura-<br>tion of 4 hours                                                           |
| MECHANICAL STORAGE:       |                                                                                                                                         |
| Crated                    | Any position                                                                                                                            |
| Uncrated                  | Upright position only                                                                                                                   |

Table 1-2. Capabilities and Limitations

|                          |                                                                                                                                                                                |
|--------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| FREQUENCY RANGE          | 118.00 to 149.95 mc (Part No. 97870-0100/-0300)<br>108.00 to 118.00 mc (Part No. 97870-0200)                                                                                   |
| RECEIVER TYPE            | Single conversion superheterodyne type with crystal-controlled local oscillator                                                                                                |
| INTERMEDIATE FREQUENCY   | 17mc                                                                                                                                                                           |
| FREQUENCY STABILITY      | One, pretuned to any frequency within the frequency range of the receiver                                                                                                      |
| CHANNEL SPACING          | 50kc                                                                                                                                                                           |
| SIGNAL TYPE              | Unmodulated continuous carrier (AO), amplitude-modulated telephone (A3)                                                                                                        |
| INPUT IMPEDANCE          | 50 ohms coaxial, unbalanced                                                                                                                                                    |
| SENSITIVITY              | Minimum of 10-db signal-plus-noise to noise ratio with a 5-hard-microvolt input signal, modulated 30 percent at 400 cps                                                        |
| SELECTIVITY              | $\pm 15.1$ kc wide (min) at 6 db down; $\pm 31.5$ kc wide (max) at 60 db down                                                                                                  |
| SPURIOUS RESPONSES       | At least 80 db down                                                                                                                                                            |
| NOISE AND HUM LEVEL      | At least 35 db down when the modulation is removed from a 500- or 100,000-microvolt input signal, modulated 30 percent at 1000 cps                                             |
| AUDIO OUTPUT IMPEDANCES: |                                                                                                                                                                                |
| Main                     | 600 or 20,000 ohms                                                                                                                                                             |
| Low Level                | Nominally 600 ohms                                                                                                                                                             |
| AUDIO OUTPUT:            |                                                                                                                                                                                |
| Main                     | 0.01 to 1 watt                                                                                                                                                                 |
| Low Level                | 20 $\pm$ 3 db down from the main output                                                                                                                                        |
| AUDIO RESPONSE           | Less than 3-db total variation from 300 to 3000 cps                                                                                                                            |
| AUDIO DISTORTION         | Will not exceed 15 percent with a 1-volt rf input signal, modulated 30 percent at 400 cps, or exceed 25 percent with a 1-volt rf input signal, modulated 90 percent at 400 cps |
| AGC CONTROL              | Less than 3-db variation for input signals from 15 microvolts to 1 volt                                                                                                        |
| AGC TIME CONSTANT        | Less than 0.10 second                                                                                                                                                          |
| SQUELCH THRESHOLD        | 3 microvolts or less at maximum rf gain; not less than 60 microvolts at minimum rf gain                                                                                        |
| AMBIENT TEMPERATURES:    |                                                                                                                                                                                |
| Operating                | -29 to $\pm 60$ deg C (-20 to $\pm 140$ deg F)                                                                                                                                 |
| Nonoperating             | -54 to $\pm 71$ deg C (-65 to $\pm 169$ deg F)                                                                                                                                 |
| RELATIVE HUMIDITY        | 95 to 100 percent                                                                                                                                                              |

Table 1-3. Equipment Supplied

| OFFICIAL NOMENCLATURE  | COMMON NAME | QTY | DESCRIPTION AND PURPOSE                                                         |
|------------------------|-------------|-----|---------------------------------------------------------------------------------|
| VHF Receiver Model 792 | Receiver    | 1   | Receives amplitude-modulated radiotelephone signals within vhf frequency range. |

Table 1-4. Equipment Required But Not Supplied

| DESCRIPTION                                                                                                      | QTY |
|------------------------------------------------------------------------------------------------------------------|-----|
| Antenna, 52 ohms, frequency coverage from 116.00 to 149.95 mc                                                    | 1   |
| Remote Control Equipment, Type FSA-22 or FAA4 (or equivalent)                                                    | 1   |
| Headset, Plug-in Type                                                                                            | 1   |
| Attenuator, Fixed, Type UG-529U                                                                                  | 1   |
| Counter, Electronic, Model 524D                                                                                  | 1   |
| Converter, Frequency, Model 525B                                                                                 | 1   |
| Generator, VHF Signal, Model 608D                                                                                | 1   |
| Voltmeter, Vacuum Tube, Model 410B                                                                               | 1   |
| Oscilloscope, Type 545A                                                                                          | 1   |
| Preamplifier, Type 53/54A                                                                                        | 1   |
| Probe, Attenuator, Type P6000                                                                                    | 1   |
| Amplifier, Power, Signal Generator, Type 230-A                                                                   | 1   |
| Meter, Output Power, Type 1840A                                                                                  | 1   |
| Test Panel (see figure 5-1)                                                                                      | 1   |
| Oscillator, Audio, Model 200AB                                                                                   | 1   |
| Tester, Beta-h <sub>i</sub> , Model KT-1                                                                         | 1   |
| Coaxial test cable, 3 ft long, RG-58/U with UG-88C/U connectors at both ends                                     | 1   |
| Coaxial test cable, 2 ft long, RG-58/U with a UG-88C/U connector at one end and two alligator clips at other end | 1   |
| Capacitor, Mica, 10 uuf                                                                                          | 1   |
| Capacitor, Mica, 1000 uuf                                                                                        | 1   |
| Capacitor, Electrolytic, 10 uf                                                                                   | 1   |
| Adapter, Connector, Type UG-349A/U                                                                               | 2   |
| Adapter, Connector, Type UG-606/U                                                                                | 1   |



## CHAPTER 2

### INSTALLATION

2-1. INTRODUCTION. This chapter gives you the information you will need to locate and install the receiver as an operating unit. Section I gives the data you will need for effective installation planning. Section II lists equip-

ment supplied and provides logistic data. Section III gives the installation instructions. Section IV gives the instructions on how to prepare the equipment for reshipment.

## SECTION I

### INSTALLATION PLANNING

#### 2-2. LOCATION OF EQUIPMENT.

2-3. The receiver is designed to be installed in a fixed-field radio installation. The receiver must be housed in an enclosed area and be provided with a primary power source, antenna, headset, interconnecting cables, remote control equipment, and loudspeaker.

#### 2-4. RECEIVER MOUNTING REQUIREMENTS.

2-5. The receiver is designed to be mounted in a standard 19-inch equipment rack. Mounting data and space requirements for installing the receiver in the equipment rack can be determined from the receiver outline dimensions shown in figure 2-1. Ample space should be provided at the rear of the equipment rack for installation of interconnecting cables, and a minimum space of 12 inches should be provided at the front of the rack for installation and removal of the receiver.

#### 2-6. POWER REQUIREMENTS.

2-7. The receiver can be operated on either ac or dc primary power. Receiver power requirements are as follows:

115 or 230 volts ac,  $\pm 10$  percent; 50 to 400 cps;  
three-wire single phase; 27 watts maximum

or

27.5 volts dc,  $\pm 10$  percent; 14 watts maximum

2-8. Primary ac power should be obtained from base or commercial sources. Primary dc power should be obtained from a battery or dc generator. If desired, the ac and dc power sources can be connected to the receiver simultaneously, with the dc source serving as an auxiliary supply. When a battery is connected as the auxiliary supply and the receiver is operating from the ac source, the battery receives a constant trickle charge from the power supply in the receiver.

#### Caution

When using an ac power source and a dc generator simultaneously, the dc generator *must* be turned on during the time that the ac power is turned on, or the dc generator will place an excessive load on the receiver power supply. This may damage the power supply or cause fuse F2 to blow.

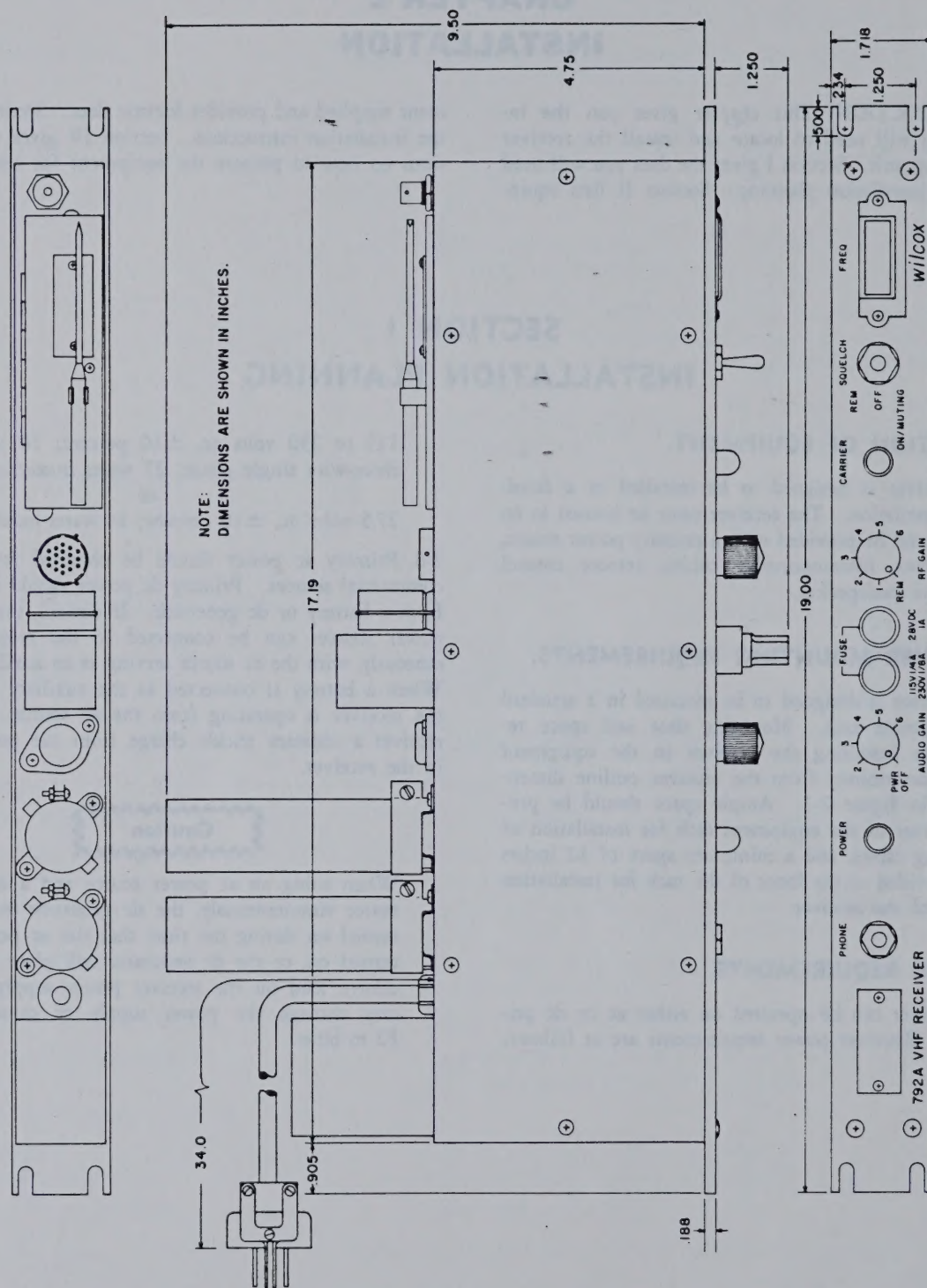


Figure 2-1. VHF Receiver Model 792A, Outline Dimensions

## SECTION II

### LOGISTICS

**2-9. INTRODUCTION.** This section lists equipment supplied and gives packing material handling instructions. Information concerning fabrication of interconnecting cables is also given.

#### **2-10. EQUIPMENT SUPPLIED.**

2-11. The receiver is supplied as a single unit in one shipping case as listed in table 2-1.

#### **2-12. MATERIAL HANDLING.**

2-13. The receiver may be transported over short distances without crating and is small enough for transport in light vehicles.

#### **2-14. CABLE REQUIREMENTS.**

2-15. The antenna cable and the interconnecting cable between the receiver and remote control circuits, loudspeaker, and the primary dc power source are not supplied and must be fabricated. The ac power cable is supplied as part of the equipment and is permanently attached to power transformer T3 in the receiver.

#### **2-16. CABLE FABRICATION.**

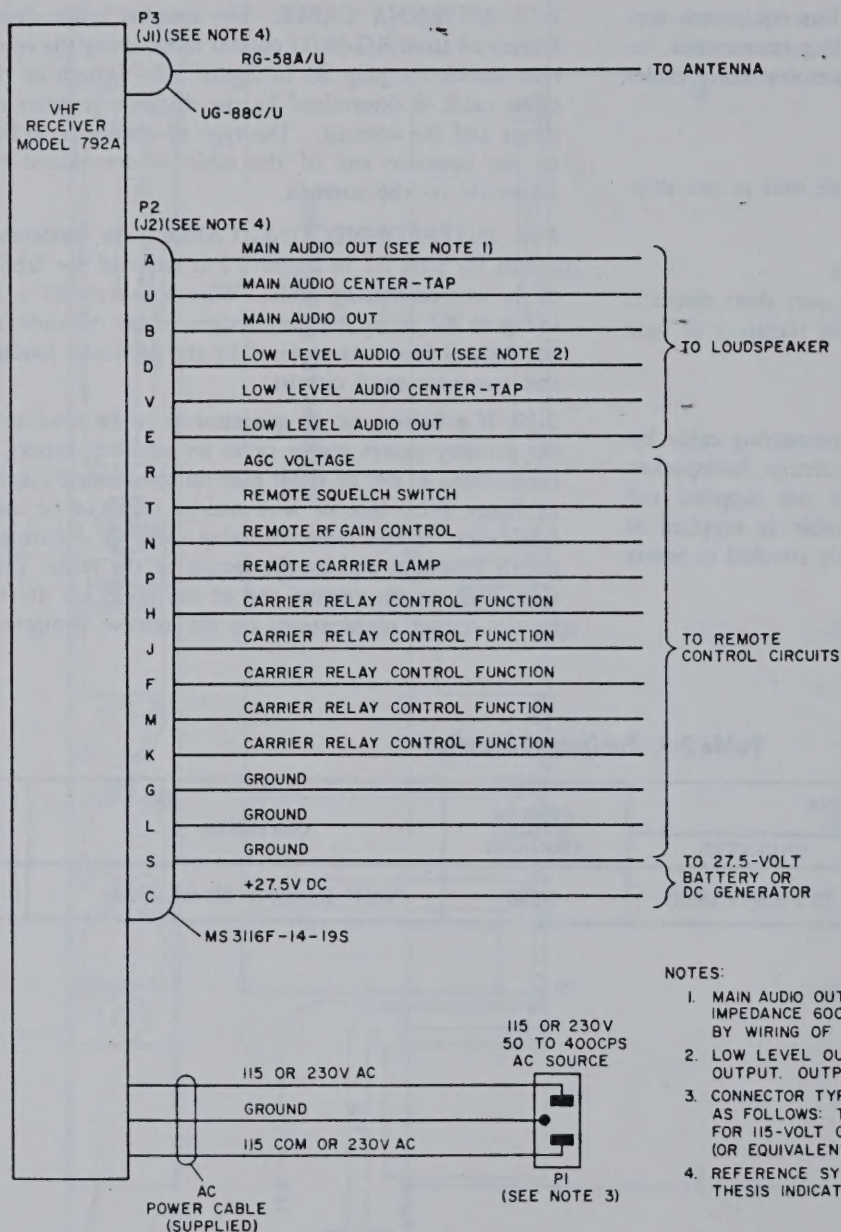
2-17. **ANTENNA CABLE.** The antenna cable should be fabricated from RG-58/U coaxial cable, using the connector type shown for plug B3 in figure 2-2. Length of the antenna cable is determined by the distance between the receiver and the antenna. The type of connector to be used on the opposite end of the cable is determined by the receptacle on the antenna.

2-18. **INTERCONNECTING CABLE.** The connector type shown for plug P2 in figure 2-2 is required for fabrication of the interconnecting cable. Wire connector P2 as shown in figure 2-2, using the pins designated for the audio output and control functions required by the particular loudspeaker and remote control circuits.

2-19. If a battery or dc generator is to be used as either the primary power source or as an auxiliary supply, make connections to the dc input pins on connector P2 as shown in figure 2-2. Size of wire and/or cable to be used for fabrication of the interconnecting cable is determined by circuit functions and by the length of the cable. Terminations used on the remote end of the cable are determined by the output terminations on the remote equipment.

*Table 2-1. Equipment Supplied*

| BOX NO. | DIMENSIONS (INCHES) |                     | CRATED WEIGHT (POUNDS) | CONTENTS                | QTY |
|---------|---------------------|---------------------|------------------------|-------------------------|-----|
|         | CRATED              | UNCRATED            |                        |                         |     |
| 1/1     | 5 x 13 x 20.5       | 1.72 x 9.50 x 19.00 | 16.00                  | VHF Receiver Model 792A | 1   |



## NOTES:

1. MAIN AUDIO OUTPUT IS 0.01 TO 1 WATT. OUTPUT IMPEDANCE 600 OR 20,000 OHMS AS DETERMINED BY WIRING OF RECEIVER TRANSFORMER T5.
2. LOW LEVEL OUTPUT IS  $20 \pm 3$  DB BELOW MAIN AUDIO OUTPUT. OUTPUT IMPEDANCE IS 600 OHMS.
3. CONNECTOR TYPE TO BE USED FOR PLUG PI IS AS FOLLOWS: TYPE UP121M PER MIL-C-3767/4 FOR 115-VOLT OPERATION; HUBBEL NO. 5674 (OR EQUIVALENT) FOR 230-VOLT OPERATION.
4. REFERENCE SYMBOL NUMBERS SHOWN IN PARENTHESIS INDICATE THE ASSOCIATE CONNECTORS.

Figure 2-2. Interconnecting Cables

## SECTION III

### INSTALLATION PROCEDURES

2-20. INTRODUCTION. This section contains the instructions you will need to install the receiver. The interconnecting cables for the receiver should be fabricated as described in Section II before starting installation procedures.

#### 2-21. GENERAL INSTALLATION INSTRUCTIONS.

2-22. CASE IDENTIFICATION. The receiver is shipped in a single case as listed in table 2-1. Report any damage of the shipping case to the responsible authority.

2-23. UNPACKING. Unpack the receiver from the shipping case just prior to installation. Save the packing list and the shipping case (together with lining and padding). Store these items for use in reshipment.

2-24. ITEM IDENTIFICATION. At the time you unpack the receiver, check the contents of the shipping case against the packing list. Report any missing or damaged items.

#### 2-25. INSTALLATION OF RECEIVER.

2-26. PRIMARY AC POWER INPUT CONNECTIONS. Transformer T3 (see figure 2-3) can be wired to permit operation of the receiver from either a 115- or 230-volt ac primary power source. The receiver is shipped from the factory with the transformer wired for 115-volt ac operation. The following procedure contains the information needed to wire the transformer for either 115- or 230-volt operation:

a. Refer to figure 2-3. Remove the eight mounting screws from the equipment cover on the top of the receiver, and remove the cover.

b. If the receiver is to be operated from a 115-volt ac

source, complete the following steps:

- (1) Wire transformer T3 (see figure 2-4) as shown in A of figure 2-5.
- (2) Obtain a 250-volt,  $\frac{1}{4}$ -ampere fuse (Type F02GR250A per MIL-F-15160). Refer to figure 2-4, and install the fuse in the holder for fuse F2.
- (3) Obtain a 115-volt power connector (Type UP121M per MIL-C-3767/4). Install the connector for plug P1 on the ac power cable as shown in figures 2-2 and 2-3.

c. If the receiver is to be operated from a 230-volt ac source, complete the following steps:

- (1) Wire transformer T3 (see figure 2-4) as shown in B of figure 2-5.
- (2) Obtain a 250-volt,  $\frac{1}{8}$ -ampere fuse (Type F02GR125A per MIL-F-15160). Refer to figure 2-4, and install the fuse in the holder for fuse F2.
- (3) Obtain a 230-volt power connector (Hubbel No. 5674, or equivalent). Install the connector for plug P1 on the ac power cable as shown in figures 2-2 and 2-3.

d. Replace the equipment cover on the top of the receiver, and secure the cover in place with the eight screws removed in preceding step a.

2-27. MAIN AUDIO OUTPUT CONNECTIONS. Transformer T5 (see figure 2-3) can be wired to provide a main audio output impedance of either 600 or 20,000 ohms. The receiver is shipped from the factory with the transformer wired for an output impedance of 600 ohms. The following

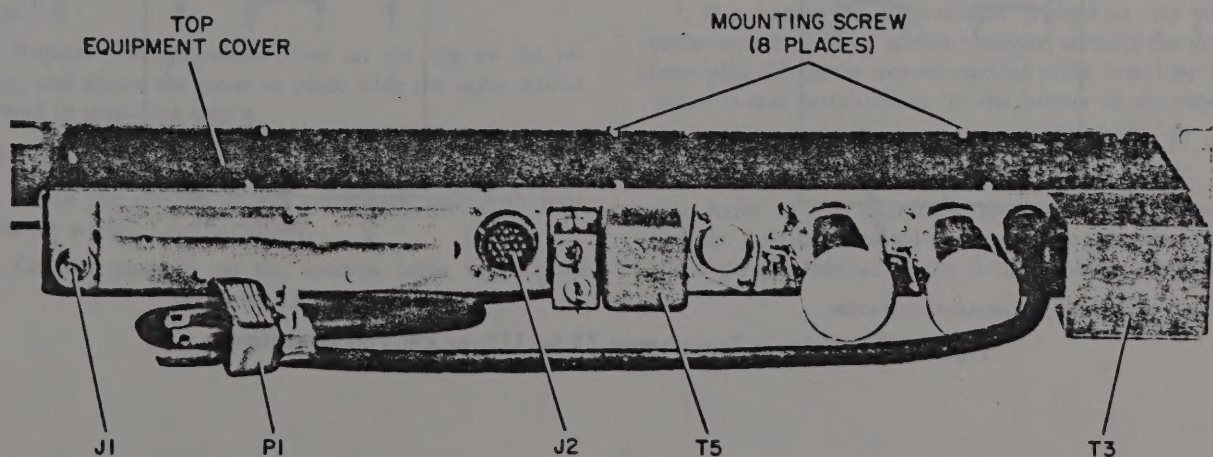


Figure 2-3. Receiver, Rear View, Parts Locations

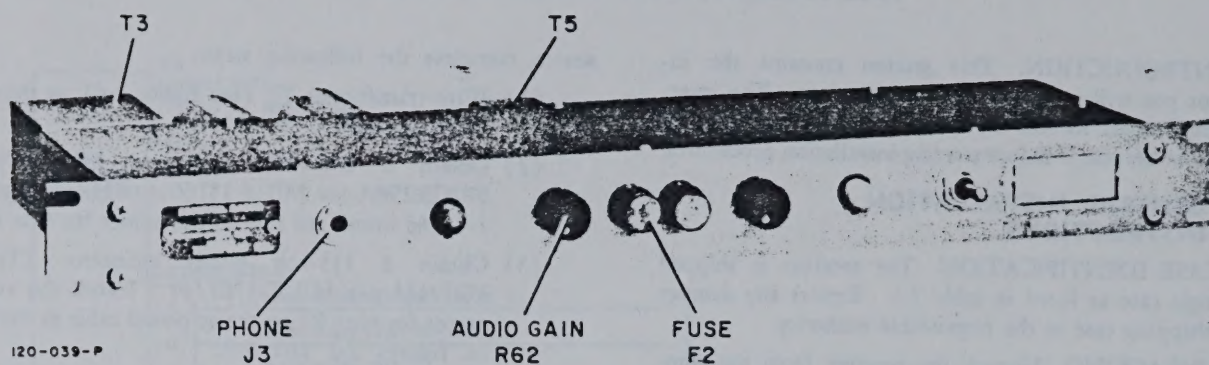
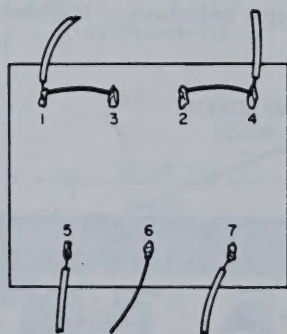
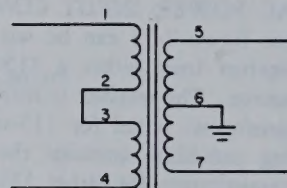
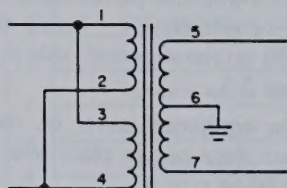
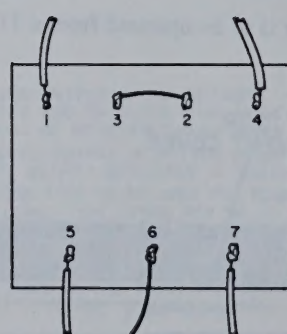


Figure 2-4. Receiver, Front View, Parts Locations



A. 115-VOLT OPERATION



B. 230-VOLT OPERATION

Figure 2-5. Wiring of Transformer T3 for 115- or 230-Volt Operation

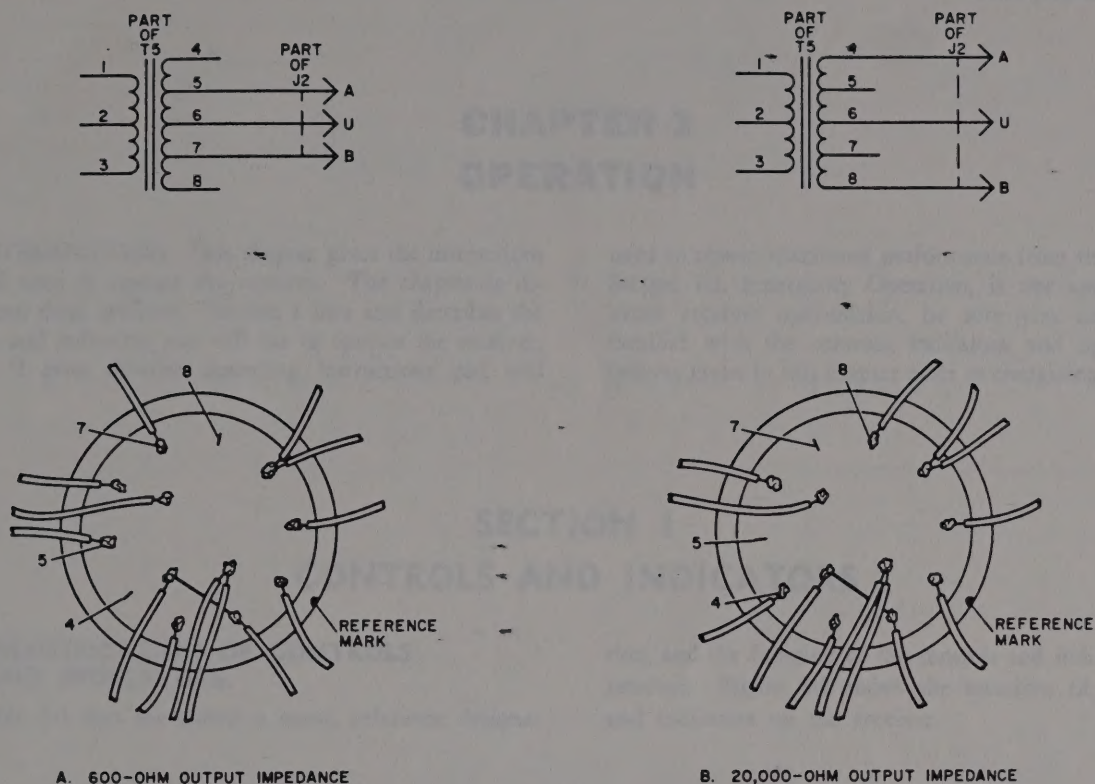


Figure 2-6. Wiring of Transformer T5 for Output Impedances of 600 or 20,000 Ohms

procedure contains the information needed to wire the transformer for either output impedance:

a. Refer to figure 2-3. Remove the eight mounting screws from the equipment cover on top of the receiver, and remove the cover.

b. If a main audio output impedance of 600 ohms is to be used, wire transformer T5 (see figure 2-4) as shown in A of figure 2-6.

c. If a main audio output impedance of 20,000 ohms is to be used, wire transformer T5 as shown in B of figure 2-6.

d. Replace the equipment cover on the top of the receiver, and secure the cover in place with the eight screws removed in preceding step a.

## 2-28. INSTALLATION PROCEDURES.

a. Install the receiver in the equipment rack with four mounting screws (not supplied).

b. Connect plug P3 on the antenna cable (see figure

2-2) to receptacle J1 on the rear of the receiver (see figure 2-3).

c. Connect plug P2 on the interconnecting cable (see figure 2-2) to receptacle J2 on the rear of the receiver (see figure 2-3).

d. Turn the receiver off by setting AUDIO GAIN control R62 (see figure 2-4) to the PWR OFF position.

e. If an ac source is used for primary power, insert plug P1 on the ac power cable (see figure 2-3) into the ac outlet.

f. If a battery or dc generator is used as the primary power source or as an auxiliary supply, connect the dc leads from plug P2 on the interconnecting cable (see figure 2-2) to the output terminations on the battery or dc generator.

g. Insert the headset plug into PHONE jack J3 on the receiver front panel (see figure 2-4).

h. Refer to the alignment procedures in Section I of Chapter 5 in this manual for instructions on how to tune and align the receiver to the desired channel frequency.

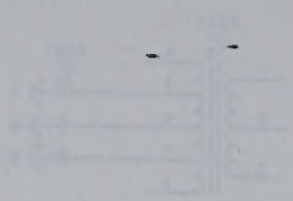
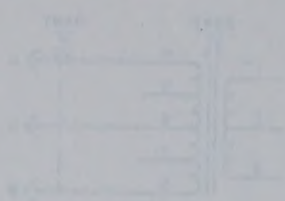


Figure 3: A diagram showing a cross-section of a structure with a central vertical axis and horizontal layers. The layers are labeled with letters A, B, C, D, E, F, G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z. The central axis is labeled 'CENTRAL AXIS'.

Figure 3: A diagram showing a cross-section of a structure with a central vertical axis and horizontal layers. The layers are labeled with letters A, B, C, D, E, F, G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z. The central axis is labeled 'CENTRAL AXIS'.

Figure 3: A diagram showing a cross-section of a structure with a central vertical axis and horizontal layers. The layers are labeled with letters A, B, C, D, E, F, G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z. The central axis is labeled 'CENTRAL AXIS'.

## CHAPTER 3

### OPERATION

3-1. INTRODUCTION. This chapter gives the instructions you will need to operate the receiver. The chapter is divided into three sections. Section I lists and describes the controls and indicators you will use to operate the receiver. Section II gives detailed operating instructions you will

need to obtain maximum performance from the equipment. Section III, Emergency Operation, is not applicable. To avoid receiver malfunction, be sure you are completely familiar with the controls, indicators, and operating procedures given in this chapter prior to energizing the receiver.

## SECTION I

### CONTROLS AND INDICATORS

#### 3-2. IDENTIFICATION OF CONTROLS AND INDICATORS.

3-3. Table 3-1 lists the common name, reference designa-

tion, and the function of the controls and indicators on the receiver. Figure 3-1 shows the locations of the controls and indicators on the receiver.

Table 3-1. Controls and Indicators

| COMMON NAME                           | REFERENCE DESIGNATION | FUNCTION                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
|---------------------------------------|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| CARRIER indicator lamp                | DS2                   | Lamp glows: <ol style="list-style-type: none"> <li>(1) When the receiver squelch circuit is disabled by setting SQUELCH switch S3 to the OFF position.</li> <li>(2) When the receiver squelch circuit is activated by setting SQUELCH switch S3 to the ON/MUTING position and the level of the receiver carrier is above the squelch threshold.</li> <li>(3) When SQUELCH switch S3 is set to the REM position and the remote squelch switch is set as described for preceding condition (1) or (2).</li> </ol> |
| POWER indicator lamp                  | DS3                   | Lamp glows whenever ac or dc power is applied to the receiver and AUDIO GAIN control R62 is turned from the PWR OFF position.                                                                                                                                                                                                                                                                                                                                                                                   |
| RF GAIN control/remote rf gain switch | R19/S1                | When SQUELCH switch S3 is set to the ON/MUTING position, control R19 adjusts threshold at which the squelch circuit opens by varying the rf gain of the receiver.<br>When SQUELCH switch S3 is set to the OFF position, control R19 adjusts the rf gain of the receiver.<br>When control R19 is set to the REM position, switch S1 transfers adjustment of the receiver rf gain to the remote rf gain control.                                                                                                  |

Table 3-1. Controls and Indicators (Cont)

| COMMON NAME                     | REFERENCE DESIGNATION | FUNCTION                                                                                                                                                                                                                                        |
|---------------------------------|-----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| AUDIO GAIN control/power switch | R62/S2                | Adjusts the level of the audio signal delivered to the headset and the audio output pins on receptacle J2. Switch S2 applies primary ac or dc power to the receiver whenever control R62 is moved from the PWR OFF position.                    |
| SQUELCH switch                  | S3                    | <p>REM position:<br/>Transfers control of receiver squelch circuit to the remote squelch switch.</p> <p>OFF position:<br/>Disables receiver squelch circuit.</p> <p>ON/MUTING position:<br/>Activates receiver squelch and muting circuits.</p> |

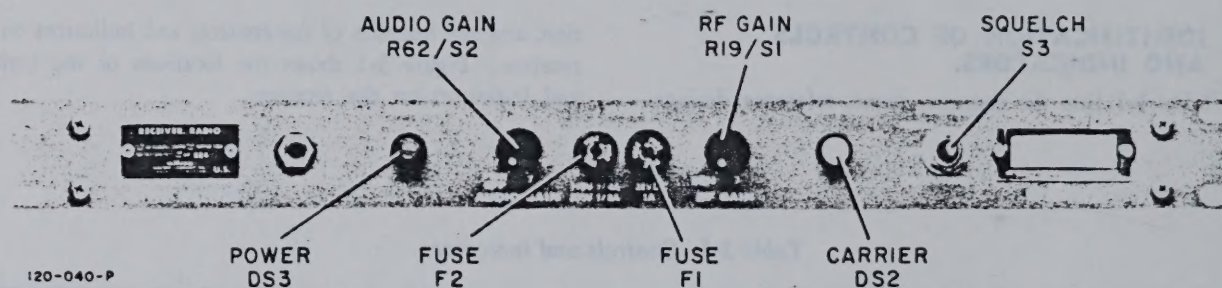


Figure 3-1. Receiver, Front Panel Control and Indicator Locations

## SECTION II

### OPERATING INSTRUCTIONS

3-4. INTRODUCTION. This section contains the instructions you will need to start, operate, and stop the receiver. In the operating procedures described in this section, the radio set is assumed to be completely installed and connected to the primary power source, antenna, headset, and remote control equipment and loudspeaker.

#### 3-5. GENERAL OPERATING INSTRUCTIONS.

3-6. The following paragraphs are applicable to both local and remote operation, except where noted. Prior to remote operation, the procedures for setting controls on the receiver front panel must be performed by the remote operator, or assistant.

#### 3-7. STARTING.

3-8. Refer to figure 3-1, and turn the AUDIO GAIN control in a clockwise direction from the PWR OFF position. The POWER indicator lamp shall glow.

#### 3-9. LOCAL OPERATION.

a. Refer to figure 3-1. Set the RF GAIN control to the full clockwise position and the AUDIO GAIN control to position 4.

b. Set the SQUELCH switch to the OFF position. Receiver background noise shall be heard in the headset, and the CARRIER indicator lamp shall glow.

c. Set the SQUELCH switch to the ON/MUTING position. If the CARRIER indicator lamp is extinguished, leave the RF GAIN control set to the full clockwise position. If the receiver is located in a noise environment sufficient to break the receiver squelch and cause the lamp to glow, adjust the RF GAIN control in a counterclockwise direction until the lamp is *just* extinguished.

d. When an amplitude-modulated signal is received, adjust the AUDIO GAIN control until the audio signals heard in the headset are at the desired listening level.

e. If desired, the receiver may be operated with the squelch circuit disabled by setting the SQUELCH switch to the OFF position.

#### 3-10. REMOTE OPERATION.

a. Refer to figure 3-1. Set the AUDIO GAIN control to position 4 and the RF GAIN control to the REM position.

b. Place the SQUELCH switch in the REM position.

#### 3-11. STOPPING.

3-12. Set the AUDIO GAIN control to the PWR OFF position. The POWER indicator lamp should be extinguished.

## SECTION III

### EMERGENCY OPERATION

NOT APPLICABLE



## CHAPTER 4

### PRINCIPLES OF OPERATION

**4-1. INTRODUCTION.** This chapter contains information that will aid you in understanding how the receiver operates. The chapter is divided into three sections. Section I describes the principles of receiver operation.

Section II describes the functional operation of the individual electronic circuits. Section III, Functional Operation of Mechanical Assemblies, is not applicable.

## SECTION I

### FUNCTIONAL SYSTEM OPERATION

#### 4-2. GENERAL

**4-3. VHF Receiver Model 792A** is a vhf unit capable of receiving amplitude-modulated signals on a preset channel in the frequency range 108.00 to 149.95 mc. The receiver consists of single-conversion superheterodyne circuitry plus auxiliary circuits. An antenna, primary power source, remote control equipment, and loudspeaker or headset are required to complete the system. Remote control and loudspeaker requirements are compatible with FSA-22 and FAA4 remote control equipment.

#### 4-4. BLOCK DIAGRAM ANALYSIS.

**4-5. GENERAL.** A functional block diagram of the receiver is shown in figure 6-1. Refer to figure 6-1 as you read the block diagram discussion.

**4-6. SIGNAL CIRCUITS.** Rf signals in the 108.00 to 149.95-mc range are received by the antenna and amplified by rf amplifiers Q1 and Q2. Transistors Q12 and Q13 function as a two-stage local oscillator that operates on a single crystal-controlled frequency below the received rf signal. Output of the local oscillator is injected into mixer Q3, where it is combined with the output of second rf amplifier Q2 to produce the receiver i-f of 17 mc. This i-f signal is amplified by four-stage amplifier Q4 through Q7 and then applied to audio and agc detector Q8.

**4-7.** When SQUELCH switch S3 is set to the ON/MUTING position, audio signals from detector Q8 pass through diode CR6 to first audio amplifier Q16 only if the received rf carrier level exceeds the squelch threshold. Otherwise, the audio signals are applied to Q16 through the muting control circuit. When SQUELCH switch S3 is set to the OFF position, the audio signals pass through diode CR6 to Q16. Audio signals at the input of Q16 are amplified by transistors Q16 and Q17

and the push-pull output amplifier consisting of transistors Q18 and Q19.

**4-8. AUXILIARY CIRCUITS.** The dc component of the detected i-f signal is amplified by first and second agc amplifiers Q9 and Q10. Dc output voltage from Q10 is applied as a forward automatic-gain-control (agc) voltage to first and second rf amplifiers Q1 and Q2 and mixer Q3. The agc voltage is also applied to squelch amplifier Q11 and to the rf gain control input.

**4-9.** The rf gain control circuit controls the gain of first i-f amplifier Q4 and, thereby, the receiver overall gain. When SQUELCH switch S3 is in the ON/MUTING position, the gain control circuit and the local (or remote) rf gain control establish the squelch threshold of the receiver by acting as an adjustable shunt attenuator for the output signal of transistor Q4. When remote rf gain switch S1 is set to the REM position, adjustment of receiver rf gain is transferred to the remote rf gain control.

**4-10.** Squelch amplifier Q11 is operated as a switch to control squelch-gate and noise-limiter diode CR6 and carrier-operated relay K1. When SQUELCH switch S3 is in the ON/MUTING position, transistor Q11 is switched 'on' only if the received rf carrier level exceeds the squelch threshold. Otherwise, Q11 is switched 'off'. Transistor Q11 is always switched 'on' when SQUELCH switch S3 is set to the OFF position. When transistor Q11 is switched 'on', diode CR6 is forward biased and relay K1 is energized. Otherwise, CR6 is reverse biased and K1 de-energized. When SQUELCH switch S3 is set to the REM position, manual control of squelch amplifier Q11 is transferred to the remote squelch switch.

**4-11.** When the receiver is operated from a 115- or 230-volt, 50- to 400-cps power source, ac voltage is applied

to the power rectifier circuit. The nominal +33-volt unregulated output of the rectifier circuit is supplied to series voltage regulator Q14, regulator amplifier Q15, the battery charger and reverse-polarity protection circuit, the coil of carrier-operated relay K1, and the indicator lamp circuits. The nominal +28-volt output of the battery charger is intended for use in trickle charging an external

battery. When the receiver is operated from a +27.5-volt source, the dc voltage is applied through the reverse-polarity protection circuit to the regulator, relay, and indicator circuits.

4-12. Transistors Q14 and Q15 are employed in a voltage regulator circuit that provides a +20-volt regulated output for use in various stages of the receiver.

## SECTION II

### FUNCTIONAL OPERATION OF ELECTRONIC CIRCUITS

#### 4-13. GENERAL.

4-14. For complete circuit details of the receiver, refer to the over-all schematic diagram in figure 6-2.

#### 4-15. CIRCUIT ANALYSIS.

#### 4-16. FIRST RF AMPLIFIER.

4-17. Signals received in the range of 108.00 to 149.95 mc are fed through a double-tuned circuit and coupled by capacitor C6 to the base of first rf amplifier Q1, which functions as a low-noise common-emitter amplifier. Q1 gain is controlled by agc voltage derived from second agc amplifier Q10. Resistor R2, in the collector circuit of Q1, provides additional forward agc control. Capacitor C7 bypasses resistor R2 over the signal frequency range. The signal from capacitor C7 is applied to the base of second rf amplifier Q2 through a single-tuned circuit and capacitor C9.

#### 4-18. SECOND RF AMPLIFIER.

4-19. Second rf amplifier Q2 is a common-emitter stage. The second rf amplifier is also controlled by agc voltage from second agc amplifier Q10. Resistor R6, in the collector circuit of Q2, provides additional forward agc control; to maintain rf linearity, resistor R6 is not bypassed. The amplified signal fed through resistor R6 from the collector of Q2 is applied to the base of mixer Q3 through a single-tuned circuit and capacitor C62. A single-tuned circuit in the outputs of the first and second rf amplifier stages, preceded by a double-tuned circuit at the input of the first rf amplifier stage, establishes an over-all front-end bandpass that provides suppression of cross-modulating signals, especially at the 0.5-megacycle points.

#### 4-20. LOCAL OSCILLATOR.

4-21. Transistors Q12 and Q13 function in a modified Butler circuit as the local oscillator. The oscillator operates 17mc below the rf carrier signal on a frequency in the range of 91.00 to 132.95 mc. Crystal Y1, a modified Type CR-56A/U low-temperature-coefficient unit, is operated at series resonance on its fifth overtone mode. Coil L11 is connected across crystal Y1 to cancel a portion of the crystal shunt capacitance. Coil L10 and variable capacitor C46 form the principal elements of the oscillator tank circuit. Capacitors C47 and C49 provide temperature compensation for the collector-to-base junction of transistor Q12. Oscillator output is extracted from voltage divider resistors R74 and R76 in the collector circuit of transistor Q13. The voltage divider maintains the proper load on the oscillator circuit and establishes the injection level for mixer Q3. The injection voltage is coupled to the emitter of

mixer Q3 through capacitor C22.

#### 4-22. MIXER.

4-23. Mixer Q3 combines the rf and injection signals to develop a difference component of 17.00 mc as the receiver intermediate frequency (i-f). Coil L7 and capacitor C21 form a series-tuned circuit at 17mc to provide a low impedance path for the i-f signal from the emitter of Q3 to ground. Mixer gain is controlled by agc voltage which is supplied to the base of transistor Q3 through a filter and decoupling network. Resistor R79 in the collector circuit of Q3 provides additional forward agc control. Capacitor C63 bypasses resistor R79 at the 17-mc i-f frequency. The i-f signal coupled through capacitor C63 is applied to the input of 17-mc crystal filter FL1 through a tuned output circuit. Capacitors C19 and C20 in the tuned circuit are arranged as a capacitive divider to present the proper driving impedance for filter FL1. Response of the filter is  $\pm 15$  kc minimum at 6 db, and  $\pm 31$  kc maximum at 60 db. FL1 output is applied to the four-stage i-f amplifier input.

#### 4-24. I-F AMPLIFIER.

4-25. FIRST I-F AMPLIFIER. The first i-f amplifier and rf gain control circuit is shown in figure 4-1. The i-f input signal from filter FL1 is coupled through tunable transformer T1 and applied to the base of first i-f amplifier Q4. Transformer T1 matches the output impedance of filter FL1 to the input impedance of transistor Q4. First i-f amplifier Q4 is an emitter-follower and its gain is controlled by the rf gain control. Attenuation provided by the rf gain control circuit is determined by the position of the local or remote rf gain control and the developed agc voltage.

4-26. Resistors R15 and R16 form a voltage divider that applies approximately +15 volts to the anode of diode CR2 in the absence of a signal and with RF GAIN control R19 set to the full clockwise position. Under these conditions, the voltage divider formed by control R19 and resistor R24 applies approximately +16 volts to the cathode of diode CR3, causing both CR2 and CR3 to be reverse biased. When remote rf gain switch S1 is placed in the REM position, the remote rf gain control is substituted for control R19. With diodes CR2 and CR3 reverse biased, the only load on first i-f amplifier Q4 is the input impedance of second i-f amplifier Q5, and the receiver is operated at maximum gain.

4-27. When RF GAIN control R19 is set to the full counterclockwise position, the voltage at the cathode of diode CR3 drops to zero, causing the anode potential on diode CR2 to follow. In this condition, diodes CR2 and CR3 are in maximum conduction. The potential on CR3 cathode is varied between 0 and approximately +16

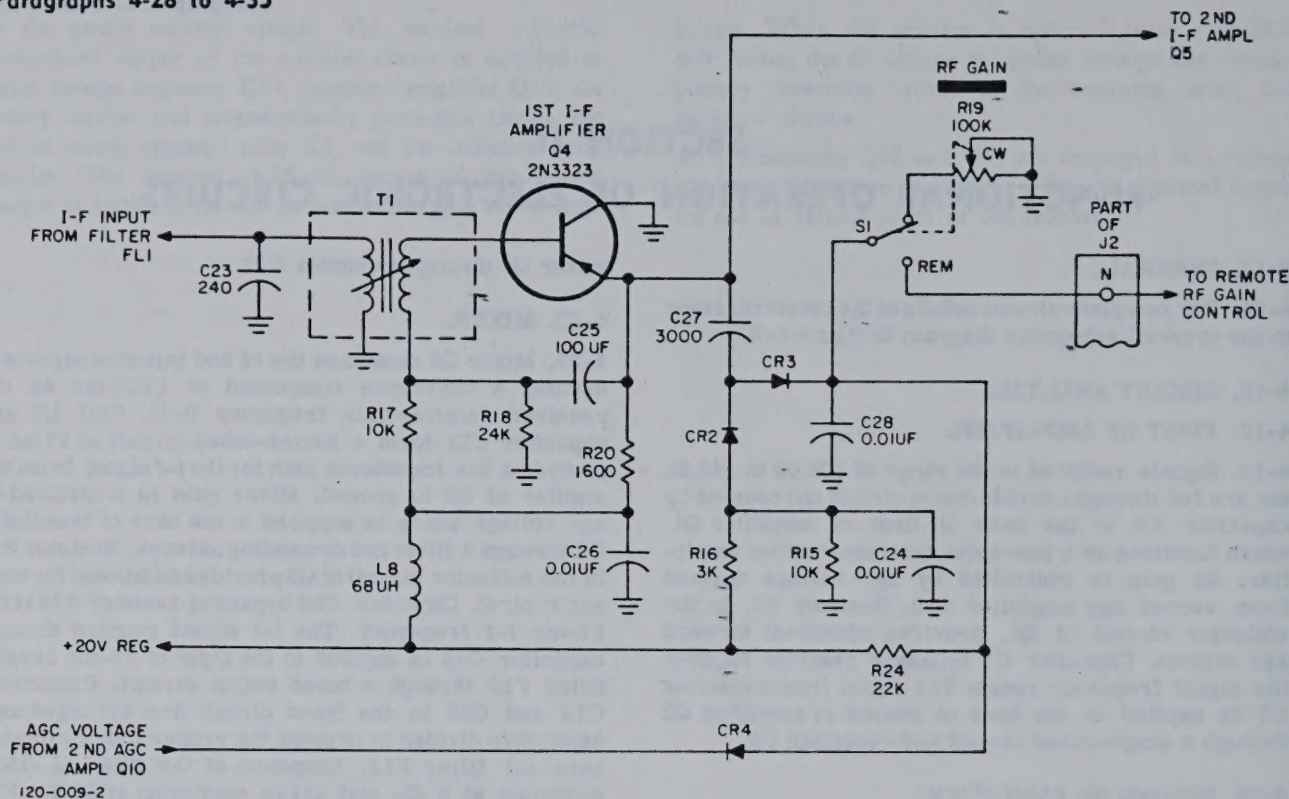


Figure 4-1. First I-f Amplifier and Rf Gain Control Circuit, Partial Schematic Diagram

volts for intermediate positions of control R19. With diodes CR2 and CR3 in conduction, the load on first i-f amplifier Q4 increases due to the lower impedance path that is formed from the emitter of Q4 to ground through capacitor C27, diodes CR2 and CR3, and capacitors C24 and C28. As the load on first i-f amplifier Q4 is increased, the gain of the receiver is reduced.

4-28. With a 3-microvolt or greater input signal and RF GAIN control R19 set to the full clockwise position, the receiver agc voltage decreases from the no-signal level of +20 volts to approximately +15 volts, causing diode CR4 to conduct. With CR4 in conduction, diodes CR2 and CR3 begin to conduct. With larger rf signal levels, the agc voltage continues to decrease, causing the conduction of diodes CR2 and CR3 to increase. Decreasing the receiver agc voltage then acts in the same manner to reduce the gain of the receiver as turning the RF GAIN control in a counterclockwise direction. When the potential on the cathode of CR3 drops below +4 volts, the agc circuits are saturated. Any further increases in rf signal level have no effect on receiver sensitivity or gain.

4-29. SECOND I-F AMPLIFIER. Second i-f amplifier Q5, driven directly from the output of first i-f amplifier Q4, is arranged in an emitter-follower configuration. Output of Q5 is applied directly to the base of third i-f amplifier Q6.

4-30. THIRD I-F AMPLIFIER. Third i-f amplifier Q6 functions as a conventional common-emitter amplifier. The output load for Q6 is the input impedance of fourth i-f amplifier Q7.

4-31. FOURTH I-F AMPLIFIER. Transistor Q7 functions in a common-emitter configuration as the fourth

(final) i-f amplifier stage. The amplified i-f output signal appearing at the collector is coupled through tunable transformer T2 to the base of audio and agc detector Q8. Transformer T2, resonated at 17 mc, aids in suppressing signals differing from the receiver i-f while matching the output impedance of Q7 to the input impedance of transistor Q8.

#### 4-32. AUDIO AND AGC DETECTOR.

4-33. Transistor Q8 functions as the audio and agc detector. Detected audio signals appearing on the emitter of Q8 are fed through a low-pass filter network consisting of resistors R29 and R80 and capacitors C57 and C35. This low-pass filter network establishes the audio response of the receiver. Audio signals appearing at the output of the filter network are coupled by capacitor C36 to squelch-gate and noise-limiter diode CR6. In part No. 097870-0300 only, the unamplified audio is connected to connector J2 pin 17 (HI F1 AUDIO). The audio signals are also applied to the input of the muting control circuit, which consists of resistor R30, muting control R36, and capacitor C39. The dc component of the detected i-f signal appearing on the collector of transistor Q8 is filtered by capacitor C34 and resistor R28 and fed through resistor R31 to the base of first agc amplifier Q9.

#### 4-34. SQUELCH GATE AND MUTING CIRCUITS.

4-35. The squelch gate and muting circuits are shown in figure 4-2. Diode CR6 serves as the squelch gate and also functions as a series noise limiter of the current-limiting type. Audio signals from the emitter of Q8 are applied to the anode of CR6. The squelch gate is opened when-

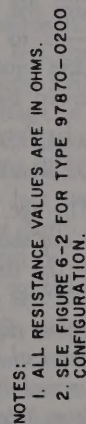


**Figure 4-2. Squelch, Muting, Agc, and Carrier-Operated Relay Circuits, Partial Schematic Diagram**

ever the level of the carrier signal exceeds the squelch threshold, or when SQUELCH switch S3 is set to the OFF position. Resistors R32 and R33 form a voltage divider which places the anode of diode CR6 at a potential of approximately +10 volts. Placing switch S3 in the OFF position causes squelch amplifier Q11 to become saturated, allowing diode CR9 to conduct. The cathode of diode CR6 is then effectively returned to ground through resistor R43, diode CR9, transistor Q11, and diode CR8. In this condition, diode CR6 is forward biased, and the receiver audio signals are coupled through the squelch gate and capacitor C38 to AUDIO GAIN control R62 in the input circuit of first audio amplifier Q16.

4-36. When SQUELCH switch S3 is set to the ON/MUTING position, squelch amplifier Q11 is turned off, and diode CR9 ceases to conduct until the level of the carrier signal reaches the squelch threshold. When diode CR9 ceases to conduct, a voltage of approximately +10 volts is obtained from the voltage divider formed by resistors R35, R38, R42, and R43 and applied to the cathode of diode CR6. This potential reverse biases diode CR6 and closes the squelch gate. The audio signals are then passed to the input of the first audio amplifier through the muting control circuit.

4-37. The muting control circuit is formed by resistor R30, muting control R36, and capacitor C39. This circuit attenuates the receiver audio output level over a range of from -6 to -30 db when the receiver is operating below the squelch threshold. The amount of attenuation is governed by control R36.

#### 4-38. AGC, SQUELCH AMPLIFIER, AND CARRIER-OPERATED RELAY CIRCUITS.

4-39. The agc, squelch amplifier, and carrier-operated relay circuits are shown in figure 4-2. Transistors Q9 and Q10 serve as the first and second agc amplifiers. The dc component of the detected i-f signal obtained from audio and agc detector Q8 is fed through resistor R31 and applied to the base of first agc amplifier Q9. The emitter voltage on Q9 is established by means of a voltage divider consisting of resistors R34, R35, R38, and R42. Resistor R38 is also connected in the collector circuit of squelch amplifier Q11 via diode CR9. The amplified dc voltage appearing on the collector of transistor Q9 is applied directly to the base of second agc amplifier Q10. The agc output voltage is taken from the emitter of transistor Q10. Capacitor C37 is connected from the emitter of Q10 to the base of first agc amplifier Q9 to utilize the voltage gain of Q9 in establishing the agc time constant. This arrangement permits the use of a relatively low capacitance value for C37. Resistor R31, capacitor C37, and the voltage gain of Q9 then become the principal elements in determining the agc time constant.

4-40. The agc voltage at the emitter of second agc amplifier Q10 is applied directly to the cathode of diode CR4 in the rf gain control circuit. This agc voltage also passes through diode CR1 and is then applied to the mixer and rf amplifier stages. Diode CR1 delays agc action in the rf and mixer stages until the i-f agc range is complete.

Resistors R10 and R11 form a voltage divider which establishes the proper base voltage for the rf and mixer stages. Resistor R77 provides isolation between the rf and mixer stages.

4-41. The agc voltage at the collector of second agc amplifier Q10 is applied directly to the base of squelch amplifier Q11. Transistor Q11 is operated as a switch to control the action of squelch-gate and noise-limiter diode CR6 and carrier-operated relay K1. Squelch amplifier Q11 is switched to a saturated state when SQUELCH switch S3 is set to the OFF position, or when switch S3 is set to the ON/MUTING position and the level of the carrier signal exceeds the squelch threshold. When SQUELCH switch S3 is set to the REM (remote) position, manual control of the state of squelch amplifier Q11 is transferred to the remote squelch switch.

4-42. Collector voltage is supplied to the coil of relay K1 from the +33-volt unregulated source in the receiver. When SQUELCH switch S3 is set to the OFF position, a dc current path is completed from the +20-volt regulated source to ground through resistor R41, diode CR7, and resistor R40, applying forward bias to squelch amplifier Q11 and saturating the amplifier. When SQUELCH switch S3 is set to the ON/MUTING position, a ground is applied to the anode of diode CR7, preventing diode CR7 from conducting and causing the base voltage on transistor Q11 to fall to zero. In this condition, transistor Q11 is reverse biased and ceases to conduct until the level of the carrier signal reaches the squelch threshold. When the squelch threshold is reached, the second agc amplifier collector current flowing through resistor R40 provides a positive base voltage for squelch amplifier Q11. This voltage is of sufficient magnitude to cause diode CR8 to conduct and saturate the squelch amplifier.

4-43. With squelch amplifier Q11 saturated, the squelch gate is opened, and carrier-operated relay K1 is energized. The operating point of relay K1 can be adjusted over a range of approximately 3 to 60 microvolts by changing the receiver rf gain with either the local or remote rf gain control. When relay K1 is energized, voltage is supplied from the +33-volt unregulated source through relay contacts 15 and 16 to CARRIER indicator lamp DS2 and to the remote 'carrier' lamp. Resistors R55 and R58 drop the voltage applied to the two lamps to a potential less than +28 volts. Additional contacts are provided on K1 for control of other (optional) external functions.

#### 4-44. AUDIO AMPLIFIER.

4-45. A three-stage audio amplifier is employed in the receiver. Audio signals from either the squelch gate or muting circuit are fed through AUDIO GAIN control R62 and resistor R63 and coupled by capacitor C53 to the base of first audio amplifier Q16. Diode CR16 provides temperature compensation for the emitter-to-base junction of transistor Q16. Capacitor C55 serves as an audio bypass for emitter resistor R67. Resistor R65 provides negative voltage feedback from the collector of Q16 to the base to provide both ac and dc stability. Over-all negative feedback from the output of the three-stage amplifier is applied to

the collector of Q16 through resistor R68. The signal at the collector of Q16 is applied directly to the base of the second audio amplifier.

4-46. Transistor Q17 functions as the second audio amplifier. Capacitor C56 serves as the audio emitter bypass. Capacitor C64 provides negative feedback from collector to base. The amplified signal at the collector of Q17 is coupled through transformer T4 and applied to the audio output amplifier.

4-47. Transistors Q18 and Q19 operate in a push-pull, Class B circuit as the audio output amplifier. Audio input signals are coupled to the bases of Q18 and Q19 via the tapped secondary winding of transformer T4. Resistors R71 and R72 provide base bias. Diode CR17 provides temperature compensation for the emitter-to-base junction of each of the output transistors. Resistor R73 provides dc stability for the amplifier. Collector voltage is supplied to transistors Q18 and Q19 from the center-tapped primary winding of audio output transformer T5. The second primary winding of transformer T5 supplies over-all negative feedback for the three-stage amplifier. This feedback voltage is supplied to the collector of first audio amplifier Q16 through resistors R69 and R68. Resistor R66 and capacitor C54, in conjunction with the feedback winding of transformer T5, establish the level of the feedback voltage. The feedback voltage stabilizes the over-all gain and decreases the output impedance of the amplifier to reduce the effect of load variations.

4-48. Audio output signals from Q18 and Q19 are coupled through transformer T5 to the remote headset or loudspeaker. Two receiver audio output circuits are provided by the two secondary windings contained on transformer T5. Connections to the two windings are terminated at pins on receptacle J2. The two output circuits are designated as the main audio output circuit and the low-level audio output circuit. The main audio output circuit will deliver a minimum of 1 watt into either a 600- or 20,000-ohm load, depending on the connections made to the taps on the main secondary winding of the output transformer. The receiver is shipped from the factory with connections to taps 5 and 7 on the main secondary winding, to provide a main audio output impedance of 600 ohms. Taps 4 and 8 provide a main audio output impedance of 20,000 ohms. The low-level audio output circuit will deliver into a 600-ohm load a signal that is  $20 \pm 3$  db down from the main output, regardless of whether a 600- or a 20,000-ohm load is selected for the main output. The low-level audio output circuit is also capable of delivering a minimum of 1 watt into a 6-ohm load if the main audio output load is open-circuited. PHONE jack J3 is connected across the low-level output circuit to provide a means of monitoring the receiver output with a headset. The main audio output and the low-level audio output are both controlled by AUDIO GAIN control R62, which is located on the receiver front panel.

#### 4-49. POWER SUPPLY.

4-50. The receiver is normally operated from a 115-volt, 50- to 400-cps, ac source, but facilities are provided for

operation from a 230-volt, 50- to 400-cps, ac source or a 27.5-volt battery or dc generator. A battery charging circuit is included in the receiver to permit trickle charging of an external 27.5-volt battery from the primary ac power source. If desired, the ac and dc power sources can be connected to the receiver simultaneously, with the dc source serving as an auxiliary supply. With the equipment connected in this manner, the receiver is automatically powered from the auxiliary supply in the event of an interruption of the ac power. When a battery is connected as the auxiliary supply and the receiver is operating from the ac source, the battery receives a constant trickle charge from the power supply in the receiver.

#### 4-51. PRIMARY POWER AND RECTIFIER CIRCUITS.

The receiver is shipped from the factory with power transformer T3 wired for 115-volt operation. Power switch S2 controls the application of ac and/or dc power to the receiver. When the receiver is operated from an ac power source, the ac voltage is applied to the primary of power transformer T3 through connector P1 and switch S2. Fuse F2 provides protection during ac operation. The ac voltage is stepped down through transformer T3 to approximately 80 volts, rectified by diodes CR11 and CR12, and filtered by capacitors C50 and C51 to provide a nominal +33-volt unregulated dc output. This unregulated voltage is supplied to the coil of carrier-operated relay K1 and to the input of the voltage regulator circuit. Resistor R56 drops the unregulated voltage to a potential of less than +28 volts for operation of POWER indicator lamp DS3 on the receiver front panel. The unregulated voltage is also applied to contact number 16 on relay K1. When relay K1 is energized, the voltage is fed through contacts 16 and 15 of the relay, dropped by resistor R55 to less than +28 volts, and applied to CARRIER indicator lamp DS2 on the receiver front panel. Resistor R58 provides a similar voltage drop for operation of the remote squelch lamp.

4-52. When the receiver is operated from an external 27.5-volt dc supply, the dc voltage is applied from pin C of connector J2 to the regulator, relay, and indicator circuits through switch S2. Fuse F1 provides protection during dc operation. Diode CR10 protects the receiver against reverse-polarity input voltage. Resistor R59 provides surge-current protection for fuse F1 and capacitors C50 and C51.

4-53. BATTERY CHARGER CIRCUIT. During ac operation, a nominal 27.5-volt dc output voltage is supplied to pin C of connector J2 through resistors R59 and R57 for trickle charging of an external 27.5-volt battery.

4-54. VOLTAGE REGULATOR. A voltage regulator circuit utilizing transistors Q14 and Q15 is employed in the receiver to provide a +20-volt regulated output for various stages throughout the unit. With primary power applied to the receiver, current flows through resistor R61 and Zener diode CR15 from the +33-volt unregulated supply in the unit. This current develops a voltage across diode CR15 which forward biases regulator amplifier Q15. Zener diode CR15 is connected from the base of Q15 to ground to establish a +20-volt dc reference for the regulator circuit. The collector current of transistor Q15 is used to base-bias

series regulator Q14. The collector of transistor Q14 is also connected to the receiver load. The base current of transistor Q15 is determined by the difference between the Zener reference and the regulated output voltage. The base current of Q15, in turn, determines the base and collector current of series regulator Q14. When the regulated output voltage at the collector of Q14 tends to decrease due to load changes, the base current of Q15 increases, maintaining the regulated voltage essentially constant. The

regulator circuit is insensitive to changes in input voltage as long as this voltage remains greater than the reference voltage established by diode CR15. These input voltage changes only alter the collector voltages of transistors Q15 and Q14 and do not affect the base currents of the two transistors. Diode CR14 provides a base-to-emitter voltage drop for transistor Q14 to insure that transistor Q14 does not over-conduct during light load conditions.

## SECTION III FUNCTIONAL OPERATION OF MECHANICAL ASSEMBLIES

NOT APPLICABLE

### SECTION I ORGANIZATIONAL FIELD MAINTENANCE

#### 5-2. SCOPE

5-2.1. This section of the manual contains the instructions for test and adjustment of the receiver and the test equipment listed in Table 5-1. Instructions of all equipment, with maintenance procedures, are included in the maintenance manual for the receiver in Table 5-2.

#### 5-3. TEST EQUIPMENT REQUIRED

5-3.1. Table 5-1 lists the test equipment that is required as part of the receiver test kit. You will also be required to use the equipment listed in the maintenance manual for the receiver.

#### 5-4. RECEIVER FIELD MAINTENANCE

5-4.1. Field maintenance procedures for the receiver include the use of test equipment and procedures and instructions, visual inspection, and cleaning. Test procedures for the receiver are listed in the following table. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

#### 5-5. MAINTENANCE PROCEDURES

5-5.1. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

#### 5-6. TEST POINTS

5-6.1. Table 5-2 lists the test points for the receiver. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

#### 5-7. RECEIVER PERFORMANCE STANDARDS

5-7.1. The following test procedures are used to check the receiver performance. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.2. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.3. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

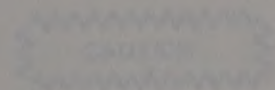
5-7.4. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.5. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.6. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.7. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.8. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.



5-7.9. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.

5-7.10. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition. The receiver is a mechanical device and its performance is dependent on its physical condition.



## CHAPTER 5

### MAINTENANCE

**5-1. INTRODUCTION.** This chapter contains the information you will need to maintain the receiver in proper working order. These instructions are based on the premise that you understand the theory of operation and purpose of the receiver and that you have sufficient experience to perform standard maintenance procedures with the test

equipment listed in this chapter. Section I of this chapter gives you instructions for organizational/field maintenance procedures. Complete performance test data is given in Section I to insure that the receiver is operating within acceptable standards. The instructions for Section II are the same as those for Section I.

## SECTION I

### ORGANIZATIONAL/FIELD MAINTENANCE

#### 5-2. SCOPE.

**5-3.** This section of the technical manual provides instructions for test and alignment of the receiver with the test equipment listed in table 5-1. In performance of the organizational/field maintenance procedures, you will find it necessary to refer to the schematic diagram in figure 6-2.

#### 5-4. TEST EQUIPMENT REQUIRED.

**5-5.** Table 5-1 lists the test equipment that is not supplied as part of the receiver but which you will need to perform the organizational/field maintenance instructions in this section.

#### 5-6. PREVENTIVE MAINTENANCE

**5-7.** Preventive maintenance procedures for the receiver include the use of basic standards of good practice and workmanship, visual inspection, and cleanliness. Preventive maintenance is the best procedure for keeping the receiver up to maximum performance. Maintenance of appropriate records is important. You may find in reviewing these records that the trouble you are encountering is indicated by previous maintenance.

#### 5-8. MAINTENANCE PRACTICES.

**5-9.** The prime responsibility for the service record of the receiver is assigned to organizational/field maintenance personnel. Your ability to keep the receiver in proper working order is dependent on how well you evaluate information obtained from all reports and tests. Keep an accurate log of all maintenance and include pertinent meter readings taken under normal operating conditions. This information will help localize trouble quickly during testing procedures.

#### 5-10. TEST POINTS.

**5-11.** Table 5-2 lists test points used in test and alignment procedures of this section. Test points are listed in numerical order and are identified by location and circuit function. Test points physical locations are shown in figures 5-3, 5-4, 5-5, 5-7, and 5-9; electrical locations are shown on the schematic diagram in figure 6-2.

#### 5-12. MINIMUM PERFORMANCE STANDARDS.

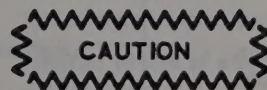
**5-13.** Use the following test procedures to determine if the receiver meets minimum performance standards. To assure maximum performance of the receiver, the tests should be performed at regular intervals, as specified by the using command, and also after completing test, alignment, and/or repair procedures on a malfunctioning equipment. The tests, when used with the block and schematic diagrams in Chapter 6, also serve to localize trouble in a malfunctioning equipment to a particular section or circuit.

**5-14.** Perform the following test procedures in sequence for a complete performance check of the receiver. When testing a particular circuit for minimum performance standards, perform the preparatory procedures in paragraph 5-15, the paragraphs pertaining to the circuit under test, and the final procedure in paragraph 5-22.

**5-15. PREPARATORY PROCEDURES.** Complete the following steps to prepare the receiver for minimum performance tests:

a. Refer to figure 3-1, and make the following switch and control settings on the receiver front panel.

- (1) AUDIO GAIN control to the PWR OFF position.
- (2) RF GAIN control to full clockwise position.
- (3) SQUELCH switch to ON/MUTING position.



If part No. 097870-0100 receiver is to be tested, set test panel TYPE switch to 1. If part No. 097870-0200/-0300 receiver is to be tested, set test panel TYPE switch to 2.

b. Connect the primary power source to the receiver. If an ac source is to be used for primary power, insert plug P1 on receiver ac power cable into ac outlet. Refer to Chapter 2, Section III, for information pertaining to 115- or 230-volt operation. If a 27.5-volt

Table 5-1. Test Equipment Required

| TEST EQUIPMENT                                                             | COMMON NAME           | MANUFACTURER                                   | REQUIRED CHARACTERISTICS                                                                                                                                                                                                                                                                           |
|----------------------------------------------------------------------------|-----------------------|------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Headset, Plug-in Type                                                      | Headset               |                                                | Impedance: 600 ohms.                                                                                                                                                                                                                                                                               |
| Attenuator, Fixed, UG-529U                                                 | 6-db Attenuator       |                                                | Impedance: 52 ohms.<br>Attenuation: 6 db.                                                                                                                                                                                                                                                          |
| Counter, Electronic, Model 524D                                            | Frequency Counter     | Hewlett-Packard Co., Palo Alto, California     | Freq Range: 10 cps to 10 mc.<br>Input Voltage: 1 volt rms min.<br>Input Impedance: 1 megohm shunted by 40 uuf.                                                                                                                                                                                     |
| Converter, Frequency, Model 525B (used with Model 524D Electronic Counter) | Frequency Converter   | Hewlett-Packard Co., Palo Alto, California     | Freq Range: 100 mc to 220 mc.<br>Input Voltage: 0.1 volt rms min.                                                                                                                                                                                                                                  |
| Voltmeter, Vacuum Tube, Model 410B                                         | Vtvm                  | Hewlett-Packard Co., Palo Alto, California     | Dc Voltage Ranges: 0-1, 3, 10, 30, 100, 300, and 1000 at 100 megohms.<br>Ac Voltage Ranges (rms): 0-1, 3, 10, 30, 100, and 300 at 10 megohms or less, shunted by 1.3 uuf.<br>Resistance Ranges: 0.2-500, 5K, 50K, 500K, 5 meg, 50 meg, and 500 meg.<br>Freq Response: Flat within +1 db to 700 mc. |
| Generator, VHF Signal, Model 608D                                          | Signal Generator      | Hewlett-Packard Co., Palo Alto, California     | Freq Range: 10 to 480 mc.<br>Output Voltage: 0.1 microvolt to 500 millivolts.<br>Impedance: 50 ohms.<br>Modulation: Internal 400- and 1000-cps sine wave or by externally applied signal.                                                                                                          |
| Oscilloscope, Type 545A                                                    | Oscilloscope          | Tektronix Inc, Beaverton, Oregon               | Sensitivity: 0.05 volt/cm max.<br>Freq Range: Dc to 30 mc. Down 3 $\pm 0.5$ db at 30 mc.<br>Input Impedance: 10 megohms, 11.5 uuf (with P6000 probe).<br>Sweep Freq Range: Dc to 30 mc.                                                                                                            |
| Preamplifier, Type 53/54A (used with 545A oscilloscope)                    | Preamplifier          | Tektronix Inc, Beaverton, Oregon               | Sensitivity: 0.005 volt/cm max.<br>Freq Range: Dc to 20 mc.<br>Input Impedance: 10 megohms, 11.5 uuf (with P6000 probe).                                                                                                                                                                           |
| Probe, Attenuator, Type P6000                                              | 10:1 Attenuator Probe | Tektronix Inc, Beaverton, Oregon               | Attenuation: 10:1<br>Impedance: 10 megohms, 11.5 uuf.                                                                                                                                                                                                                                              |
| Amplifier, Power, Signal Generator, Type 230-A                             | Rf Amplifier          | Boonton Radio Corp., Boonton, New Jersey       | Bandwidth: 10 to 500 mc.<br>Voltage Gain: 34 db.<br>Output Voltage: 10 volts peak.<br>Impedance: 50 ohms input and output.                                                                                                                                                                         |
| Meter, Output Power, Type 1840-A                                           | Power Output Meter    | General Radio Co., West Concord, Massachusetts | Impedance Range: 2.5 to 20,000 ohms.<br>Power Range: 0.1 milliwatt to 50 watts, $\pm 2$ percent, from 50 to 8000 cps.                                                                                                                                                                              |

Table 5-1. Test Equipment Required (Cont)

| TEST EQUIPMENT                                        | COMMON NAME        | MANUFACTURER                                | REQUIRED CHARACTERISTICS                                                                                                                                                                                                   |
|-------------------------------------------------------|--------------------|---------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Oscillator, Audio, Model 200AB                        | Audio Oscillator   | Hewlett-Packard Co., Palo Alto, California  | Freq Range: 20 to 40,000 cps.<br>Output Power: 1 watt into a 600-ohm load.<br>Internal Impedance: 75 ohms (approx) from 20 to 20,000 cps.<br>Output Impedance: 600 ohms.<br>Hum Output: At least 66 db below rated output. |
| Test Panel (to be constructed as shown in figure 5-1) | Test Panel         |                                             |                                                                                                                                                                                                                            |
| Tester Beta-h <sub>ie</sub> , Model KT-1              | Test Set           | Baird-Atomic, Inc, Cambridge, Massachusetts | I <sub>CB0</sub> Scale: 5-50 ua.<br>I <sub>E</sub> Scale: 0.5-10-ma.<br>V <sub>C</sub> Ranges: 0-1.5, 3, 4.5, 6, and 7.5 volts.<br>Beta Ranges: 0-100 and 200.                                                             |
| Test Cable, Coaxial                                   | Coaxial Cable      |                                             | 3 ft lg, RG-58/U with UG-88C7U connector at both ends.                                                                                                                                                                     |
| Test Cable, Coaxial                                   | Coaxial Cable      |                                             | 2 ft lg, RG-58/U with a UG-88C/U connector at one end and two alligator clips at other end.                                                                                                                                |
| Capacitor, Mica                                       | 10 uuf Capacitor   |                                             | 10 uuf.                                                                                                                                                                                                                    |
| Capacitor, Mica                                       | 1000 uuf Capacitor |                                             | 1000 uuf.                                                                                                                                                                                                                  |
| Capacitor, Electrolytic                               | 10 uf Capacitor    |                                             | 10 uf.                                                                                                                                                                                                                     |
| Adapter, Connector, UG-349A/U                         | UG-349A/U Adapter  |                                             | Impedance: 50 ohms.<br>Configuration: straight type BNC (male) to type N (female).                                                                                                                                         |
| Adapter, Connector, UG-606/U                          | UG-606/U Adapter   |                                             | Impedance: 50 ohms.<br>Configuration: straight type BNC (female) to type N (female).                                                                                                                                       |

Table 5-2. Test Points

| TEST POINT | LOCATION                        | CIRCUIT                     |
|------------|---------------------------------|-----------------------------|
| TP1        | Rf and Mixer Subassembly        | 1st rf amplifier input      |
| TP2        | Rf and Mixer Subassembly        | Emitter of 1st rf amplifier |
| TP3        | Rf and Mixer Subassembly        | 2nd rf amplifier output     |
| TP4        | Rf and Mixer Subassembly        | Emitter of 2nd rf amplifier |
| TP5        | Rf and Mixer Subassembly        | Base of mixer               |
| TP6        | Rf and Mixer Subassembly        | Emitter of mixer            |
| TP7        | Rf and Mixer Subassembly        | Agc voltage                 |
| TP8        | Oscillator Circuit Board 117580 | Local oscillator            |
| TP9        | Oscillator Circuit Board 117580 | Output of Oscillator        |
| TP10       | Oscillator Circuit Board 117580 | Ground                      |

Table 5-2. Test Points (Cont)

| TEST POINT | LOCATION                                    | CIRCUIT                             |
|------------|---------------------------------------------|-------------------------------------|
| TP11       | Oscillator Circuit Board 117580             | Local oscillator                    |
| TP12       | Oscillator Circuit Board 117580             | Local oscillator                    |
| TP13       | I-f Circuit Board 117583                    | 1st i-f amplifier                   |
| TP14       | I-f Circuit Board 117583                    | 1st i-f amplifier                   |
| TP15       | I-f Circuit Board 117583                    | 3rd i-f amplifier                   |
| TP16       | I-f Circuit Board 117583                    | 4th i-f amplifier                   |
| TP17       | I-f Circuit Board 117583                    | Agc voltage                         |
| TP18       | I-f Circuit Board 117583                    | Audio and agc detector              |
| TP19       | I-f Circuit Board 117583                    | 2nd agc amplifier                   |
| TP20       | I-f Circuit Board 117583                    | Squelch amplifier                   |
| TP21       | I-f Circuit Board 117583                    | Squelch gate input                  |
| TP22       | I-f Circuit Board 117583                    | 1st audio amplifier input           |
| TP23       | Power Supply and Audio Circuit Board 117587 | 2nd audio amplifier output          |
| TP24       | Power Supply and Audio Circuit Board 117587 | Base of audio output amplifier      |
| TP25       | Power Supply and Audio Circuit Board 117587 | Base of audio output amplifier      |
| TP26       | Power Supply and Audio Circuit Board 117587 | Collector of audio output amplifier |
| TP27       | Power Supply and Audio Circuit Board 117587 | Collector of audio output amplifier |
| TP28       | Power Supply and Audio Circuit Board 117587 | Unregulated dc supply bus           |
| TP29       | Power Supply and Audio Circuit Board 117587 | Series voltage regulator            |
| TP30       | Power Supply and Audio Circuit Board 117587 | Regulator amplifier                 |
| TP31       | Power Supply and Audio Circuit Board 117587 | +20 volt regulated bus              |

battery or dc generator is to be used as the primary power source, refer to figure 5-1 and connect the power source to terminal board TB1 on the test panel as follows: positive lead to terminal 1 and negative lead to terminal 2. Turn the primary power source on.

c. Connect the test equipment to the receiver as shown in figure 5-2.

d. Make the following switch and control settings on the test panel.

- (1) REMOTE RF GAIN control to the full clockwise position.
- (2) REMOTE SQUELCH switch to the OFF position.
- (3) LOW LEVEL AUDIO LOAD switch to the OFF position.
- (4) MAIN AUDIO LOAD switch to the OFF position.

e. Set the load selector switch on the power output meter to the 600-ohm position and the scale multiplier to the 100 position (full scale reading of five watts).

f. Set the receiver AUDIO GAIN control to position 1. The POWER indicator lamp on the receiver shall glow.

The test panel AGC meter shall indicate 20 volts,  $\pm 5$  percent.

g. Refer to figure 5-3, use the vtvm, and measure the voltage from test point TP31 to chassis ground. The vtvm shall indicate +20 volts dc,  $\pm 5$  percent.

h. Turn the signal generator on and adjust to the channel frequency of the receiver.

#### 5-16. AUDIO POWER OUTPUT.

a. Set the signal generator for a 1000-microvolt output signal, modulated 30 percent at 1000 cps.

b. Set the receiver AUDIO GAIN control to position 5.

c. Readjust the signal generator frequency control for a maximum output indication on the power output meter.

d. The power output meter shall indicate 1 watt or greater.

e. Disconnect the power output meter from the MAIN AUDIO jacks on the test panel, and reconnect to the LOW LEVEL jacks on the test panel. Set the scale multiplier to the 0.1 position (5 milliwatts full scale).

f. Disconnect the headset from the receiver. The power output meter indication shall be 1 milliwatt or greater.

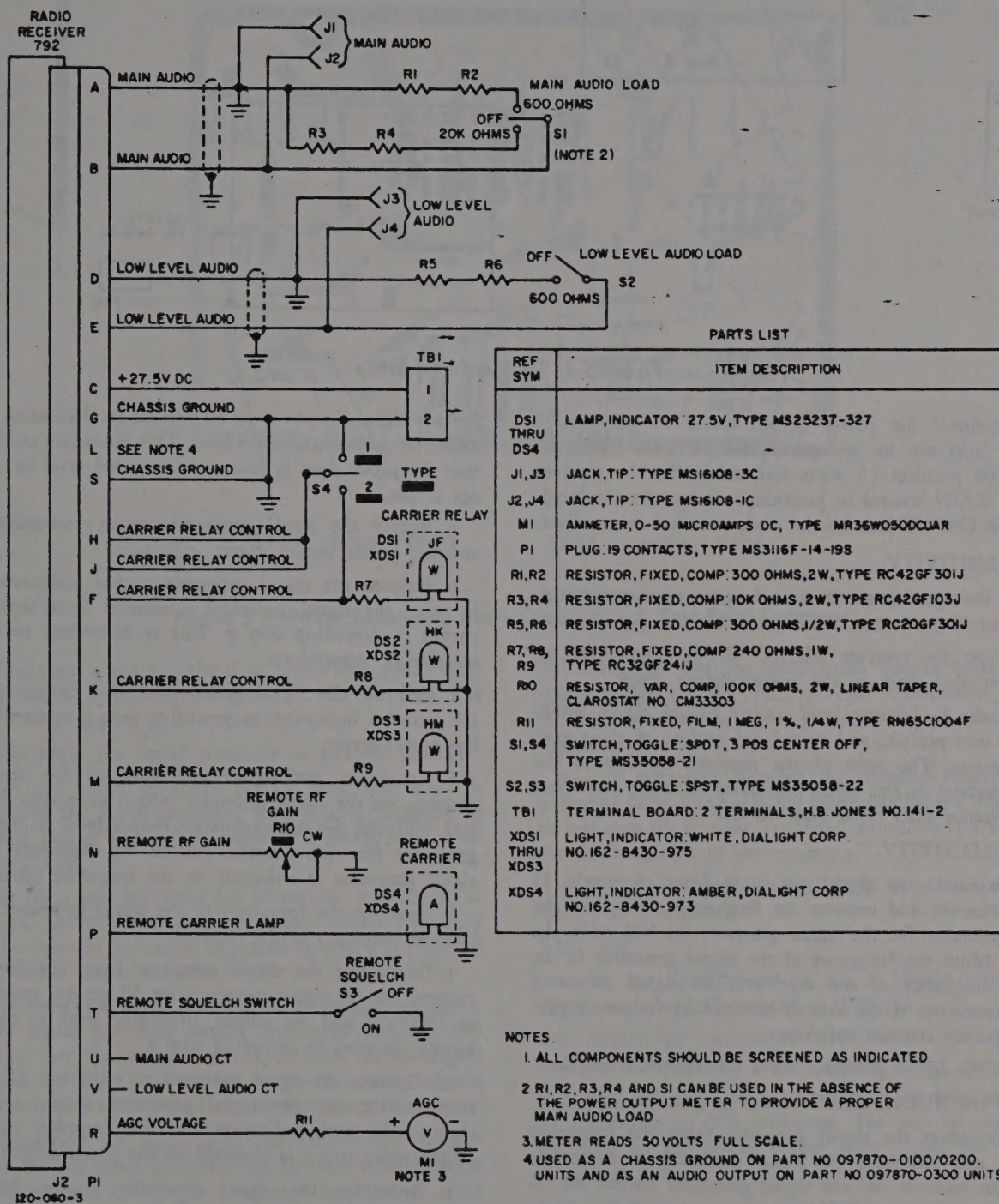


Figure 5-1. Test Panel, Schematic Diagram

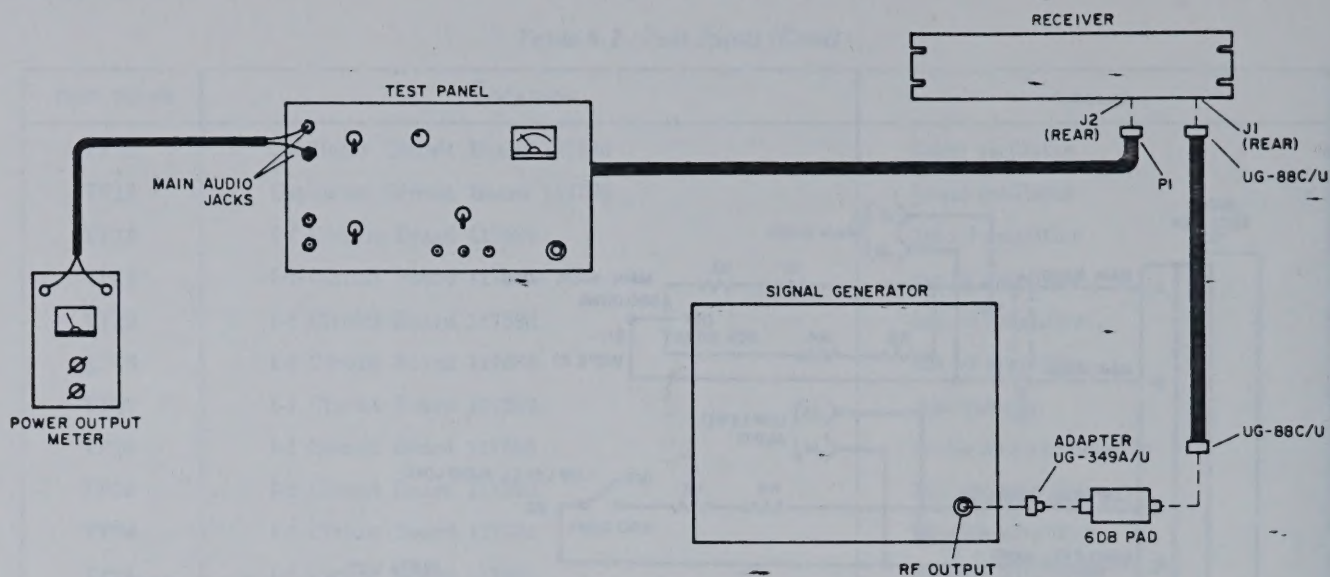


Figure 5-2. Standard Test Setup

g. Reconnect the power output meter to the MAIN AUDIO jacks on the test panel. Set the scale multiplier to the 100 position (5 watts full scale). Set the receiver AUDIO GAIN control to position 1. Reconnect the headset to the PHONE jack on the front panel of the receiver.

#### 5-17. SENSITIVITY.

a. Set the signal generator for a 5-microvolt output signal, modulated 30 percent at 1000 cps.

b. Adjust the receiver AUDIO GAIN control for an indication of 1 watt on the power output meter.

c. Set the modulation level control on the signal generator for zero percent, and record the reading of the power output meter. The ratio of the recorded reading to the 1-watt reading is the signal-plus-noise to noise ratio and the difference shall be at least 10 db.

#### 5-18. SELECTIVITY.

a. Disconnect the signal generator from receptacle J1 on the receiver and connect the frequency counter to the signal generator. Set the signal generator for 500 millivolts output. Adjust the frequency of the signal generator to the channel frequency of the receiver. The signal generator output frequency is the sum of the mixing frequency plus the frequency counter indication.

b. Set the signal generator for a 0.1-microvolt output.

c. Set the AUDIO GAIN control to position 5.

d. Disconnect the signal generator from the frequency counter, and reconnect the signal generator to receptacle J1 on the receiver. Set the signal generator output for 30 percent modulation at 1000 cps.

e. Set the SQUELCH switch to the ON/MUTING position. Slowly increase the signal generator output until the CARRIER indicator lamp is illuminated.

f. Note the audio power as indicated on the power output meter. Set the SQUELCH switch to the OFF position.

Reduce the audio power to 50 percent by decreasing signal generator output attenuator level. This procedure established that the input level is below the required level to initiate agc action.

g. Record the signal generator output attenuator level and the power output meter indication.

h. Increase the signal generator output attenuator level to twice the signal generator attenuator level setting recorded in preceding step g. This is equivalent to a 6-db increase in signal level.

i. Decrease the signal generator output frequency until the recorded indication in preceding step g is obtained on the power meter.

j. Reconnect the signal generator to the frequency counter. Set the signal generator output attenuator level to 500 millivolts. Set the modulation output level on the signal generator for zero percent. Record the frequency of the signal generator as indicated on the frequency counter.

k. Readjust the frequency of the signal generator to the channel frequency of the receiver.

l. Disconnect the signal generator from the frequency counter. Set the signal generator for 30 percent modulation at 1000 cps and the output attenuator level to the level setting recorded in preceding step b.

m. Connect the signal generator to receptacle J1 on the receiver. Increase the signal generator output frequency through the peak indication until the recorded indication of preceding step g is obtained on the power output meter.

n. Reconnect the signal generator to the frequency counter INPUT jack. Set the signal generator output attenuator level to 500 millivolts. Set the modulation output level on the signal generator for zero percent. Record the frequency of the signal generator as indicated on the frequency counter.

o. The difference between the recorded frequencies of

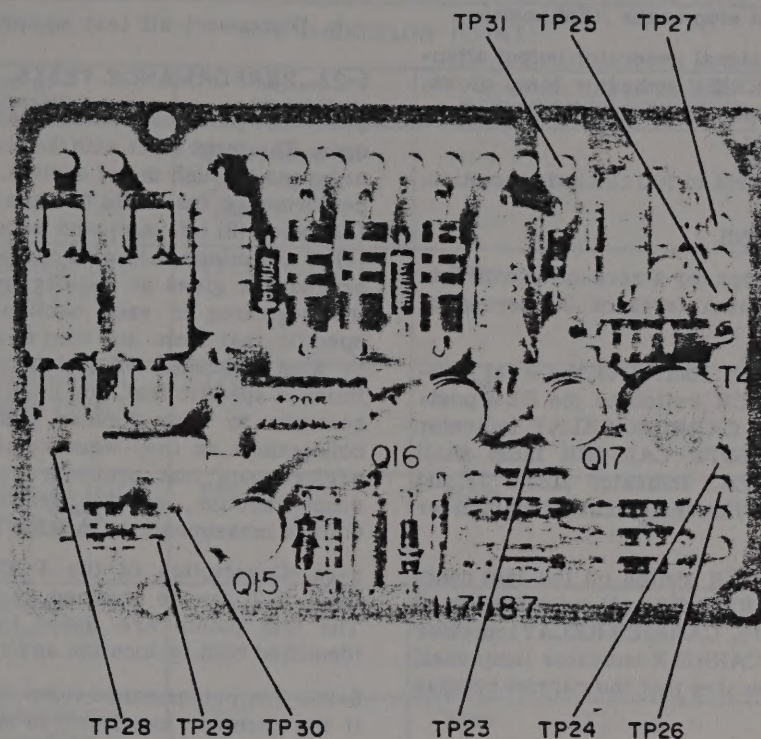


Figure 5-3. Power Supply and Audio Circuit Board, Test Point Locations

preceding steps *j* and *n* is the 6-db bandwidth, which shall be at least 30.2 kc wide (centered  $\pm 15.1$  kc from the channel frequency of the receiver).

p. Reconnect the signal generator to receiver jack J1.

q. Set the signal generator output attenuator level to 1000 times the signal generator level setting recorded in preceding step *g*. This is equivalent to a 60-db increase in the signal level.

r. Set the signal generator for 30 percent modulation at 1000 cps. Decrease the signal generator output frequency until the recorded indication in preceding step *g* is obtained on the power output meter.

s. Reconnect the signal generator to the frequency counter. Set the signal generator output attenuator level to 500 millivolts. Set the modulation output level on the signal generator for zero percent. Record the frequency of the signal generator as indicated on the frequency counter.

t. Disconnect the signal generator from the frequency counter. Set the signal generator for 30 percent modulation at 1000 cps and the output attenuator level to the level setting recorded in preceding step *q*.

u. Connect the signal generator to receptacle J1 on the receiver. Increase the signal generator output frequency until the recorded indication in preceding step *g* is obtained on the power output meter.

v. Reconnect the signal generator to the frequency counter. Set the signal generator output attenuator level to

500 millivolts. Set the modulation output level on the signal generator for zero percent modulation. Record the frequency of the signal generator as indicated on the frequency counter.

w. The difference between the recorded frequencies of preceding steps *s* and *v* is the 60-db bandwidth, which shall be less than 63 kc wide (centered within  $\pm 31.5$  kc of the channel frequency of the receiver).

x. Return the receiver SQUELCH switch to the ON/MUTING position.

#### 5-19. AGC.

a. Set the signal generator for a 15-microvolt output signal, modulated 30 percent at 1000 cps.

b. Adjust the receiver AUDIO GAIN control for an indication of 0.5 watt on the power output meter.

c. Vary the signal generator output attenuator level from 15 microvolts to 500 millivolts. The receiver output, as indicated on the power output meter, shall not vary by more than 3 db.

#### 5-20. SQUELCH.

a. Set the signal generator for a 0.1-microvolt output signal, modulated 30 percent at 1000 cps.

b. Slowly increase the signal generator output attenuator level until the receiver CARRIER indicator lamp is illuminated. The signal generator output attenuator level shall not be greater than 3 microvolts.

c. Turn receiver RF GAIN control in a counter-clockwise direction until it is against the stop. Do not turn RF GAIN control past stop to the REM position.

d. Slowly increase the signal generator output attenuator level until the CARRIER indicator lamp glows. The signal generator output attenuator level shall be greater than 60 microvolts.

e. Return RF GAIN control to full clockwise position.

#### 5-21. REMOTE FUNCTIONS.

a. Set the signal generator for a zero-microvolt output signal and a modulation level of 30 percent at 1000 cps.

b. Set receiver RF GAIN control to the REM position and receiver SQUELCH switch to the REM position. On the test panel, CARRIER RELAY indicator lamp HM and the REMOTE CARRIER lamp shall illuminate. CARRIER RELAY indicator lamps JF and HK shall be extinguished. This indicates that the carrier relay is energized.

c. Set the REM SQUELCH switch on the test panel to the ON position. CARRIER RELAY indicator lamps JF and HK shall illuminate, CARRIER RELAY indicator lamp HM and the REM CARRIER indicator lamp shall be extinguished. This indicates that the carrier relay is not energized.

d. Set the RF GAIN control on the test panel to the maximum clockwise position.

e. Slowly increase the signal generator output attenuator level for an indication on the power output meter. The signal generator output attenuator level shall not be greater than 3 microvolts; test panel indicator lamps shall be as indicated in preceding step b.

#### 5-22. FINAL PROCEDURE.

a. Set receiver AUDIO GAIN control to the PWR

OFF position, and turn the test equipment and primary power source off.

b. Disconnect all test equipment from the receiver.

#### 5-23. PERFORMANCE TESTS.

5-24. The performance tests use signal injection techniques. The tests start with the receiver audio section and progress through the rf section. The tests described in performance test table 5-3 are divided into four parts: (1) audio, (2) i-f, agc, and squelch, (3) oscillator, and (4) rf and mixer. After the completion of the preliminary procedures given at the beginning of the table and at the beginning of each section of the table, make a specific test from any step in the table without regard to what precedes or follow it. However, when you finish a specific test, you must be careful to return all controls to their former positions and restore all connections as they were before starting the test. No performance test procedure is given for the power supply section, as this section can be serviced by voltage measurements in table 5-4.

5-25. Description of the POINT OF TEST listed in table 5-3 can be obtained by reference to table 5-2. The test points are listed in numerical order and identified both by location and circuit function.

5-26. The performance tests should be performed only if the receiver has failed to meet any of the minimum performance standards in paragraph 5-12. If the receiver has failed to meet a minimum performance test, the trouble can be localized to a section in the receiver by analysis of the block diagram in figure 6-1. Then, by means of the test points listed in table 5-2 and the schematic diagram in figure 6-2, select the component or circuit you want to investigate. Refer to table 5-3 and perform the applicable test. When a defective stage is located, use normal trouble shooting techniques and the voltage measurements in table 5-4 to isolate the trouble to a specific part or parts.

Table 5-3. Performance Tests

|                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|---------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| PRELIMINARY INSTRUCTIONS: | Remove the equipment covers from the top and bottom of the receiver.<br>Perform the preparatory procedures given in paragraph 5-15.<br>After completion of performance tests, set the receiver AUDIO GAIN control to the PWR OFF position, turn the test equipment and primary power source off, and disconnect the test equipment from the receiver.                                                                                                     |
| AUDIO SECTION             |                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| PRELIMINARY INSTRUCTIONS: | Set the signal generator for zero microvolts output.<br>Connect the audio oscillator output through a 10-ufd capacitor to TP22, figure 5-4.<br>Adjust the audio oscillator for an output of 1.5 volts peak-to-peak at 1000 cps.<br>Set the receiver AUDIO GAIN control to position 3.<br>Set the oscilloscope for MAIN SWEEP NORMAL and INT. SYNC.<br>When you have completed the tests in this section, disconnect the test equipment from the receiver. |

Table 5-3. Performance Tests (Cont)

| AUDIO SECTION (CONT) |                                                                                                                                                                                                                                                                                               |                                |                                                      |                                   |
|----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------|------------------------------------------------------|-----------------------------------|
| STEP                 | OPERATION OF TEST EQUIPMENT                                                                                                                                                                                                                                                                   | POINT OF TEST                  | CONTROL SETTINGS AND OPERATION OF EQUIPMENT          | PERFORMANCE STANDARDS             |
| 1A                   | None                                                                                                                                                                                                                                                                                          | MAIN AUDIO jacks on test panel | None                                                 | 1 watt output                     |
| 1B                   | Connect the oscilloscope 10:1 attenuator probe to TP23, figure 5-3.                                                                                                                                                                                                                           | TP23                           | None                                                 | 3.5 $\pm$ 0.35 volts peak-to-peak |
| 1C                   | Connect the oscilloscope 10:1 attenuator probe to TP24, figure 5-3.                                                                                                                                                                                                                           | TP24                           | None                                                 | 1.2 $\pm$ 0.1 volts peak-to-peak  |
| 1D                   | Connect the oscilloscope 10:1 attenuator probe to TP25, figure 5-3.                                                                                                                                                                                                                           | TP25                           | None                                                 | 1.2 $\pm$ 0.1 volts peak-to-peak  |
| 1E                   | Connect the oscilloscope 10:1 attenuator probe to TP26, figure 5-3.                                                                                                                                                                                                                           | TP26                           | None                                                 | 28 $\pm$ 2.8 volts peak-to-peak   |
| 1F                   | Connect the oscilloscope 10:1 attenuator probe to TP27, figure 5-3.                                                                                                                                                                                                                           | TP27                           | None                                                 | 28 $\pm$ 2.8 volts peak-to-peak   |
| 1G                   | Disconnect the audio oscillator from TP22.<br>Reconnect the audio oscillator through a 10-ufd capacitor to TP18.<br>Adjust the audio oscillator for an output of 2.5 volts peak-to-peak.<br>At the conclusion of the test, reconnect the audio oscillator to TP22 through a 10-ufd capacitor. | MAIN AUDIO jacks on test panel | Set the receiver squelch switch to the OFF position. | 1 watt output                     |
| 1H                   | Perform step 1G.<br>Connect the oscilloscope 10:1 attenuator probe to TP21, figure 5-4.                                                                                                                                                                                                       | TP21                           | Same as 1G                                           | 1.7 $\pm$ 0.2 volts peak-to-peak  |
| 1I                   | Perform step 1G.<br>Connect the oscilloscope 10:1 attenuator probe to TP22, figure 5-4.                                                                                                                                                                                                       | TP22                           | Same as 1G                                           | 1.4 $\pm$ 0.2 volts peak-to-peak  |

Table 5-3. Performance Tests (Cont)

| OSCILLATOR SECTION                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                                                                                                                                                                                     |                                    |                                                                                                                                                                                 |                                                                         |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------|
| <b>PRELIMINARY INSTRUCTIONS:</b> Adjust the signal generator for zero microvolts output.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |                                                                                                                                                                                     |                                    |                                                                                                                                                                                 |                                                                         |
| STEP                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | OPERATION OF TEST EQUIPMENT                                                                                                                                                         | POINT OF TEST                      | CONTROL SETTINGS AND OPERATION OF EQUIPMENT                                                                                                                                     | PERFORMANCE STANDARDS                                                   |
| 2A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Set the vtvm to the 1-volt ac scale.<br>Connect the vtvm to TP8 and TP10, figure 5-5.                                                                                               | TP8                                | None                                                                                                                                                                            | 0.2 to 0.4 volt rms                                                     |
| 2B                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Connect the test equipment as shown in figure 5-6.<br>Connect the inner conductor of the RG-58/U coaxial cable through the 10-uuf capacitor to TP9 and the outer conductor to TP10. | TP9                                | None                                                                                                                                                                            | Channel frequency minus 17 mc, $\pm 0.001$ percent of channel frequency |
| I-F, AGC, AND SQUELCH SECTION                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |                                                                                                                                                                                     |                                    |                                                                                                                                                                                 |                                                                         |
| <b>PRELIMINARY INSTRUCTIONS:</b> Remove the coaxial cable that is connected between receiver receptacle J1 and the 6-db attenuator.<br>Use the RG-58/U coaxial cable that is terminated with a UG-88C/U connector on one end and two alligator clips on the other end, and connect this cable to the 6-db attenuator.<br>Connect the center conductor of the coaxial cable through a 1000-uuf capacitor to TP13, figure 5-4, and the outer conductor to chassis ground.<br>At the conclusion of the tests in this section, reconnect the test equipment as shown in figure 5-2. |                                                                                                                                                                                     |                                    |                                                                                                                                                                                 |                                                                         |
| STEP                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | OPERATION OF TEST EQUIPMENT                                                                                                                                                         | POINT OF TEST                      | CONTROL SETTINGS AND OPERATION OF EQUIPMENT                                                                                                                                     | PERFORMANCE STANDARDS                                                   |
| 3A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Set the signal generator to 17 mc, and adjust for a 20-microvolt output, modulated 30 percent at 1000 cps.                                                                          | MAIN AUDIO jacks on the test panel | Set the receiver SQUELCH switch to the OFF position.<br>Set the RF GAIN control to the maximum clockwise position.<br>Set the AUDIO GAIN control to maximum clockwise position. | 1 watt output                                                           |
| 3B                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Set the signal generator for a 50-microvolt output, modulated 30 percent at 1000 cps.<br>Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm to TP19, figure 5-4.             | TP19                               | Set the RF GAIN control to the maximum clockwise position.<br>Set the AUDIO GAIN control to position 4.<br>Set the SQUELCH switch to the ON/MUTING position.                    | $13.5 \pm 1.4$ volts dc                                                 |
| 3C                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Set the signal generator for a 50-microvolt output, modulated 30 percent at 1000 cps.<br>Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm to TP17, figure 5-4.             | TP17                               | None                                                                                                                                                                            | $15 \pm 1.5$ volts dc                                                   |

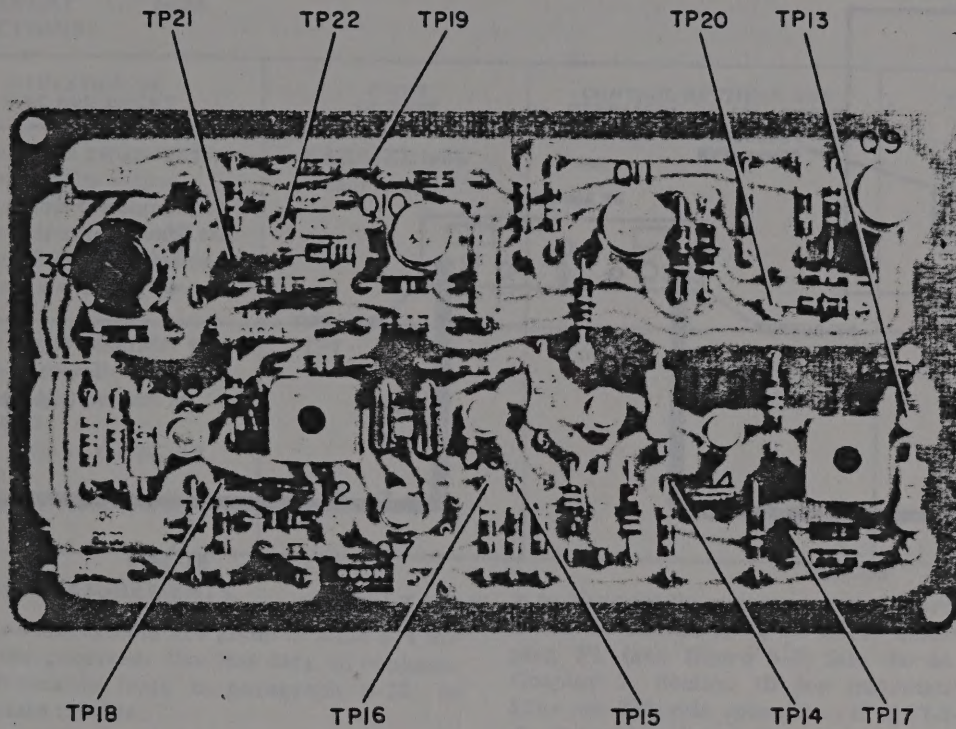


Figure 5-4. I-f and Agc Circuit Board, Test Point Locations

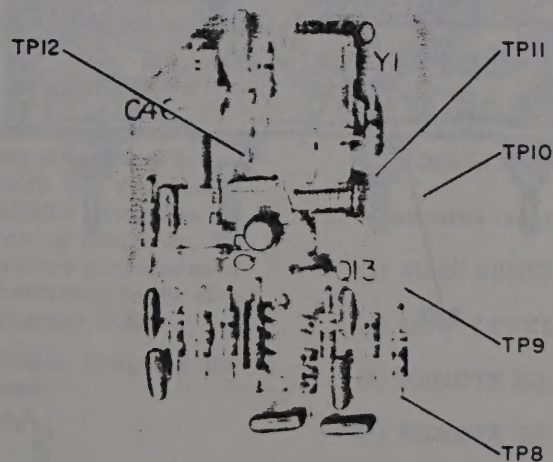


Figure 5-5. Oscillator Circuit Board, Test Point Locations

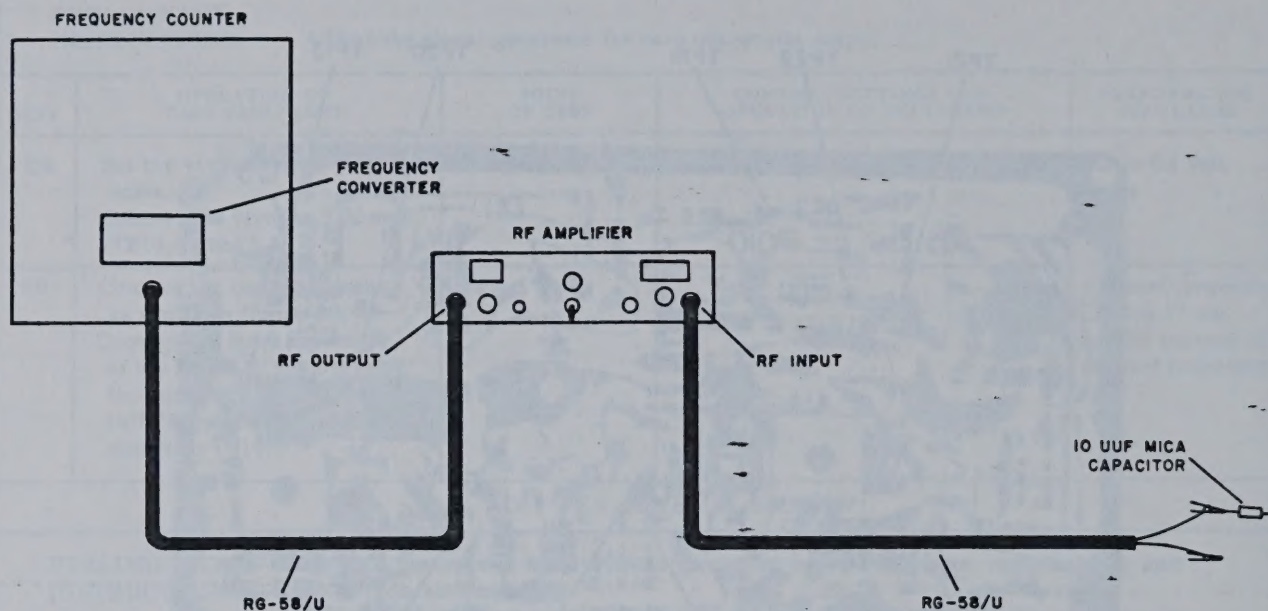


Figure 5-6. Oscillator Test Setup

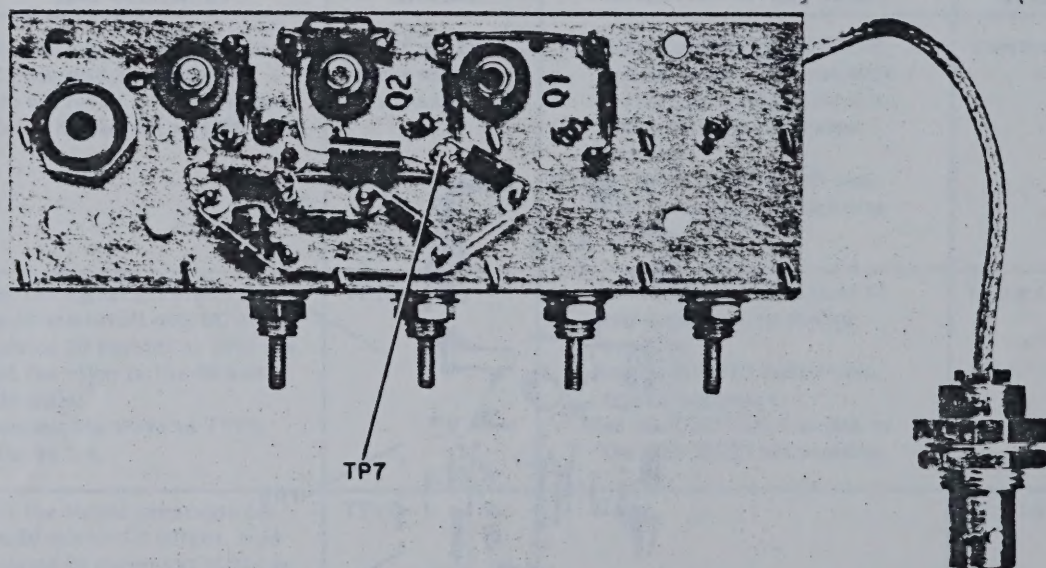


Figure 5-7. Rf Amplifier and Mixer Subassembly, Bottom View, Test Point Location

Table 5-3. Performance Tests (Cont)

| RF AND MIXER SECTION      |                                                                                                                                                                          |                            |                                             |                                                                                     |
|---------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------|---------------------------------------------|-------------------------------------------------------------------------------------|
| PRELIMINARY INSTRUCTIONS: |                                                                                                                                                                          | None.                      |                                             |                                                                                     |
| STEP                      | OPERATION OF TEST EQUIPMENT                                                                                                                                              | POINT OF TEST              | CONTROL SETTINGS AND OPERATION OF EQUIPMENT | PERFORMANCE STANDARDS                                                               |
| 4A                        | Set the signal generator for zero microvolts output. Slowly adjust the signal generator output level until the receiver CARRIER indicator lamp starts to glow.           | CARRIER indicator lamp DS2 | None                                        | The CARRIER indicator lamp shall glow with a minimum input signal of 10 microvolts. |
| 4B                        | Set the signal generator for a 1000-microvolt output, modulated 30 percent at 1000 cps.<br>Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm to TP7, figure 5-7. | TP7                        | None                                        | 9.5 $\pm$ 1 volts dc                                                                |

**5-27. VOLTAGE MEASUREMENTS.**

5-28. Voltage measurements are given in table 5-4 for test points in the receiver. Use this data, in conjunction with performance tests in paragraph 5-23, to localize and isolate trouble.

5-29. Measurements in table 5-4 are typical and are made with no signal input. Data is divided into five parts: (1) rf and mixer, (2) oscillator, (3) i-f, agc, and squelch, (4) audio, and (5) power supply. After completion of the preliminary procedures of the table and at the beginning of each section of the table, make specific measurements from any step in the table without regard to what precedes or follows. After completing a particular section, return all controls to their former positions and restore all parts and connections to starting condition.

**5-30. ALIGNMENT.**

5-31. Complete alignment of the receiver can be accomplished by performing the following procedures in sequence. Included are the steps which are required to install (or replace) the local oscillator crystal in the receiver and to align the receiver on frequency. To align a circuit, perform the preparatory procedures of paragraph 5-32, the paragraph(s) pertaining to the circuit, and the final procedure in paragraph 5-39.

5-32. PREPARATORY PROCEDURES. Complete the following steps for receiver alignment:



If part No. 097870-0100 receiver is to be aligned, set test panel TYPE switch to 1. If part No. 097870-0200/0300 receiver is to be aligned, set test panel TYPE switch to 2.

a. Connect the primary power source to the receiver. If an ac source is to be used, insert ac power cable plug P1 (see figure 5-8) into the ac outlet. Refer to Chapter 2, Section III for information pertaining to 115- or 230-volt operation. If a 27.5-volt battery or dc generator is to be used, refer to figure 5-1 and connect the power source to terminal board TB1 on the test panel as follows: positive lead to terminal 1 and negative lead to terminal 2. Turn the power on.

b. Connect the test equipment to the receiver as shown in figure 5-2.

c. Refer to figure 3-1, and make the following switch and control settings on the receiver front panel.

(1) AUDIO GAIN to PWR OFF position.

(2) RF GAIN control full clockwise.

(3) SQUELCH switch to OFF.

d. Make these test panel switch and control settings.

(1) MAIN AUDIO LOAD switch to OFF.

(2) LOW LEVEL AUDIO LOAD switch to OFF.

(3) REMOTE SQUELCH switch to OFF.

(4) REMOTE RF GAIN control full clockwise.

e. Set power output meter load selector switch to the 600-ohm position, and set the scale multiplier selector to the 100 position (full scale reading of five watts).

f. Turn the signal generator on, and set frequency

Table 5-4. Voltage Measurements

| <b>PRELIMINARY INSTRUCTIONS:</b> Remove the equipment covers from the top and bottom of the receiver.<br>Connect test panel connector P1 to receiver receptacle J2.<br>Make the following switch and control settings on the front panel of the receiver:<br>AUDIO GAIN control to the PWR OFF position, RF GAIN control to the full clockwise position, and SQUELCH switch to the OFF position.<br>Connect the receiver to the ac or dc primary power source, and turn the power source on.<br>Set the receiver AUDIO GAIN control to position 1; the POWER indicator lamp should glow and the test panel AGC meter should indicate approximately 20 volts.<br>After completing voltage measurements, set the receiver AUDIO GAIN control to the PWR OFF position.<br>Disconnect the receiver from the test panel. Replace the top and bottom equipment covers on the receiver. |                                                                                                     |               |                 |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------|---------------|-----------------|
| <b>RF AND MIXER SECTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                                                                                     |               |                 |
| <b>PRELIMINARY INSTRUCTIONS:</b> Refer to figure 5-8, and remove the rf shield cover.<br>After completing the rf and mixer voltage measurements, replace the rf shield cover.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |                                                                                                     |               |                 |
| STEP                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | OPERATION OF TEST EQUIPMENT                                                                         | POINT OF TEST | TYPICAL VOLTAGE |
| 1A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP1, figure 5-9, to chassis ground.  | TP1           | 11.5 volts      |
| 1B                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP2, figure 5-9, to chassis ground.  | TP2           | 11.7 volts      |
| 1C                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP3, figure 5-9, to chassis ground.  | TP3           | 11.5 volts      |
| 1D                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP4, figure 5-9, to chassis ground.  | TP4           | 11.9 volts      |
| 1E                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP5, figure 5-9, to chassis ground.  | TP5           | 13.5 volts      |
| 1F                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP6, figure 5-9, to chassis ground.  | TP6           | 13.8 volts      |
| 1G                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP7, figure 5-7, to chassis ground.  | TP7           | 13.2 volts      |
| <b>OSCILLATOR SECTION</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |                                                                                                     |               |                 |
| <b>PRELIMINARY INSTRUCTIONS:</b> None.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                                                                                                     |               |                 |
| STEP                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | OPERATION OF TEST EQUIPMENT                                                                         | POINT OF TEST | TYPICAL VOLTAGE |
| 2A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 3-volt dc scale.<br>Connect the vtvm from TP11, figure 5-5, to chassis ground.  | TP11          | 1.9 volts       |
| 2B                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Set the vtvm to the 10-volt dc scale.<br>Connect the vtvm from TP12, figure 5-5, to chassis ground. | TP12          | 5.2 volts       |

Table 5-4. Voltage Measurements (Cont)

| I-F, AGC, AND SQUELCH SECTION |                                                                                                   |               |                 |
|-------------------------------|---------------------------------------------------------------------------------------------------|---------------|-----------------|
| PRELIMINARY INSTRUCTIONS:     |                                                                                                   | None.         |                 |
| STEP                          | OPERATION OF TEST EQUIPMENT                                                                       | POINT OF TEST | TYPICAL VOLTAGE |
| 3A                            | Set the vtm to the 1-volt dc scale.<br>Connect the vtm from TP13, figure 5-4, to chassis ground.  | TP13          | 0 volts         |
| 3B                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP14, figure 5-4, to chassis ground. | TP14          | 15.0 volts      |
| 3C                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP15, figure 5-4, to chassis ground. | TP15          | 15.6 volts      |
| 3D                            | Set the vtm to the 10-volt dc scale.<br>Connect the vtm from TP16, figure 5-4, to chassis ground. | TP16          | 8.7 volts       |
| 3E                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP17, figure 5-4, to chassis ground. | TP17          | 15.5 volts      |
| 3F                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP18, figure 5-4, to chassis ground. | TP18          | 20 volts        |
| 3G                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP19, figure 5-4, to chassis ground. | TP19          | 20 volts        |
| 3H                            | Set the vtm to the 3-volt dc scale.<br>Connect the vtm from TP20, figure 5-4, to chassis ground.  | TP20          | 0.99 volt       |
| 3I                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP21, figure 5-4, to chassis ground. | TP21          | 9.4 volts       |
| 3J                            | Set the vtm to the 3-volt dc scale.<br>Connect the vtm from TP22, figure 5-4, to chassis ground.  | TP22          | 0 volts         |
| AUDIO SECTION                 |                                                                                                   |               |                 |
| PRELIMINARY INSTRUCTIONS:     |                                                                                                   | None.         |                 |
| STEP                          | OPERATION OF TEST EQUIPMENT                                                                       | POINT OF TEST | TYPICAL VOLTAGE |
| 4A                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP23, figure 5-3, to chassis ground. | TP23          | 19.6 volts      |
| 4B                            | Set the vtm to the 1-volt dc scale.<br>Connect the vtm from TP24, figure 5-3, to chassis ground.  | TP24          | 0.56 volt       |
| 4C                            | Set the vtm to the 1-volt dc scale.<br>Connect the vtm from TP25, figure 5-3, to chassis ground.  | TP25          | 0.56 volt       |
| 4D                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP26, figure 5-3, to chassis ground. | TP26          | 20 volts        |
| 4E                            | Set the vtm to the 30-volt dc scale.<br>Connect the vtm from TP27, figure 5-3, to chassis ground. | TP27          | 20 volts        |

Table 5-4. Voltage Measurements (Cont)

| POWER SUPPLY SECTION      |                                                                                                      |               |                           |
|---------------------------|------------------------------------------------------------------------------------------------------|---------------|---------------------------|
| PRELIMINARY INSTRUCTIONS: |                                                                                                      | None.         |                           |
| STEP                      | OPERATION OF TEST EQUIPMENT                                                                          | POINT OF TEST | TYPICAL VOLTAGE           |
| 5A                        | Set the vtvm to the 100-volt dc scale.<br>Connect the vtvm from TP28, figure 5-3, to chassis ground. | TP28          | 33 volts<br>*(27 volts)   |
| 5B                        | Set the vtvm to the 100-volt dc scale.<br>Connect the vtvm from TP29, figure 5-3, to chassis ground. | TP29          | 32 volts<br>*(26.5 volts) |
| 5C                        | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP30, figure 5-3, to chassis ground.  | TP30          | 20.8 volts                |
| 5D                        | Set the vtvm to the 30-volt dc scale.<br>Connect the vtvm from TP31, figure 5-3, to chassis ground.  | TP31          | 20 volts                  |

\* If a 27.5-volt dc power source is used, the dc voltages in parenthesis should be indicated.

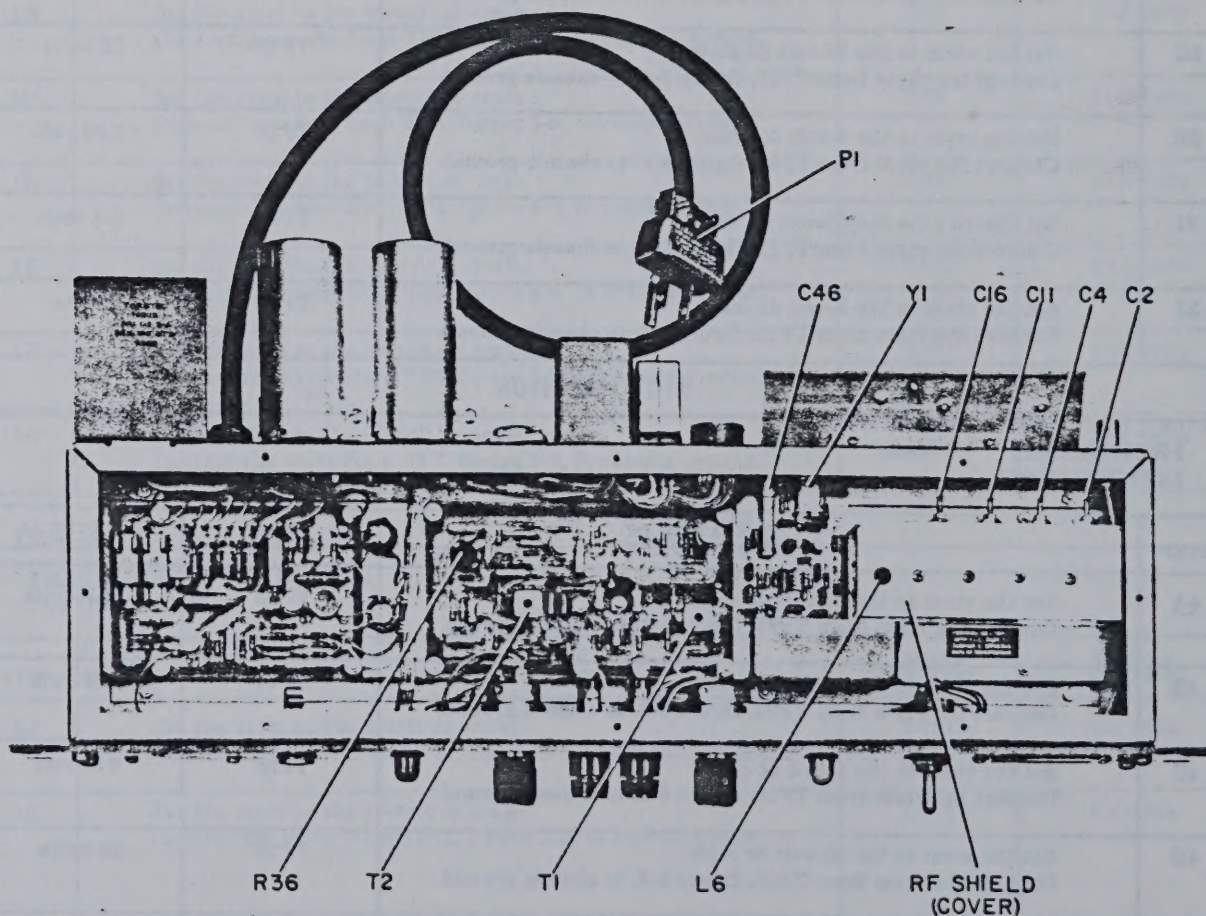


Figure 5-8. Receiver, Tuning Control Locations

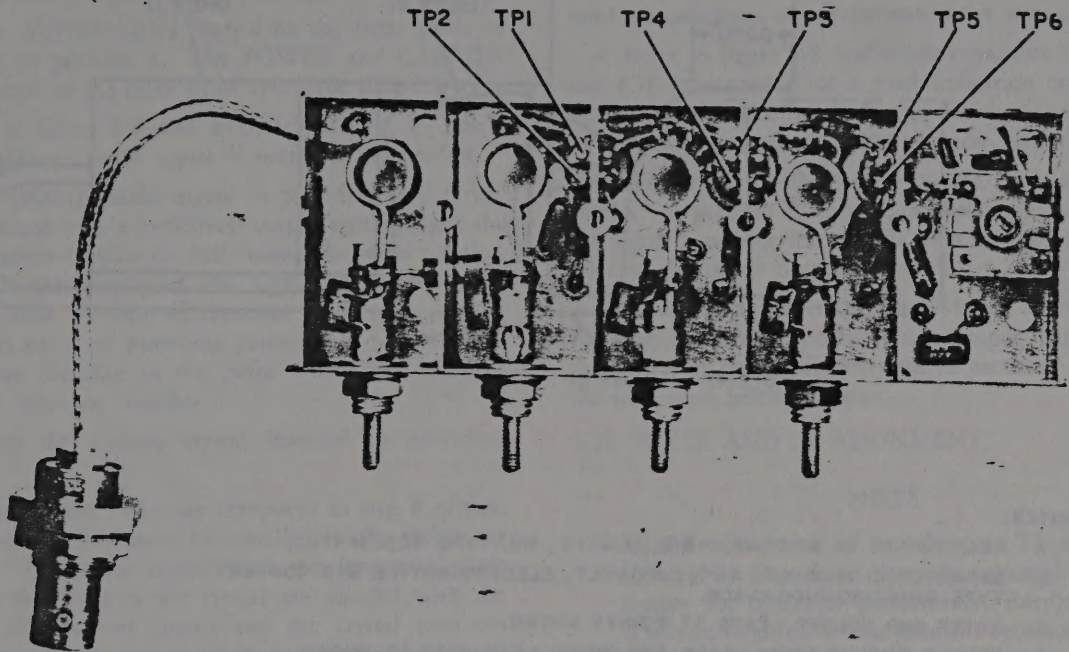


Figure 5-9. Rf Amplifier and Mixer Subassembly, Top View, Test Point Locations

of the signal generator to the channel frequency of the receiver.

g. Set the RF LEVEL control on the signal generator for maximum attenuation.

#### 5-33. PRELIMINARY RF ADJUSTMENTS.

##### CAUTION

In subsequent step *a*, do not force the slugs of capacitors C2, C4, C11, and C16 beyond the mechanical stop at the full counterclockwise position.

a. Refer to figure 5-8, and adjust the slugs of capacitors C2, C4, C11, and C16 to the full counterclockwise position.

b. Use the following formula to compute the approximate number of *clockwise* turns required to set the slugs of capacitors C2, C4, C11, and C16 to the desired channel frequency:

$$T = \frac{151 \text{ mc} - F_c}{2.5 \text{ mc}}$$

T = Approximate number of required turns.

151 mc = Receiver frequency with capacitor slugs set to the full counterclockwise position.

F<sub>c</sub> = Desired channel frequency.

2.5 mc = Frequency change for one turn of capacitor slugs.

Example: Compute the approximate number of *clockwise* turns required to set the slugs of capacitors C2, C4, C11, and C16 to a channel frequency of 132.00 mc:

$$T = \frac{151.00 - 132.00}{2.5} = 7.6$$

##### NOTE

In subsequent step *c*, do not turn the slugs of capacitors C2, C4, C11, and C16 more than 17 turns in a clockwise direction from the full counterclockwise position, or false tuning indications may be obtained.

c. Adjust the slugs of capacitor C2, C4, C11, and C16 in a clockwise direction the number of turns computed in preceding step *b*.

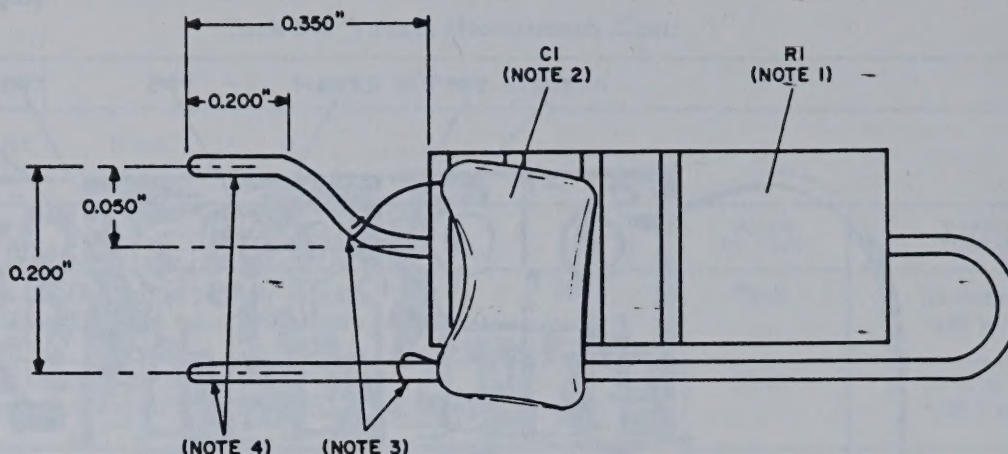
#### 5-34. PRELIMINARY OSCILLATOR ADJUSTMENT.

##### CAUTION

In subsequent step *a*, do not force the slug of capacitor C46 beyond the mechanical stop at the full counterclockwise position.

a. Refer to figure 5-8, and adjust the slug of capacitor C46 to the full counterclockwise position.

b. Use the following formula to compute the required



NOTES:

1. RESISTOR R1 IS 47 OHMS,  $\pm 5\%$ , 1 WATT, MIL TYPE RC32GF470J.
2. CAPACITOR C1 IS 15UF,  $\pm 5\%$ , 100 VOLT, ELECTRO MOTIVE MFG COMPANY TYPE DMI0C150JPI00WV4CR.
3. TWIST AND SOLDER LEADS AT POINTS SHOWN.
4. REMOVE TINNING FROM LEADS AND ROUND LEAD TIPS AS SHOWN.

Figure 5-10. Dummy Crystal, Construction Details

frequency of the local oscillator crystal at the desired channel frequency:

$$F_x = F_c - 17 \text{ mc}$$

$F_x$  = Required local oscillator crystal frequency in megacycles.

$F_c$  = Desired channel frequency.

17 mc = Receiver i-f frequency.

Example: Using 132.00 mc as the desired channel frequency, compute the required frequency of the local oscillator crystal.

$$F_x = 132.00 - 17.00 = 115.00 \text{ mc}$$

c. Use the following formula to compute the approximate number of clockwise turns required to set the slug of oscillator capacitor C46 to the crystal frequency determined in preceding step b:

$$T = \frac{134.00 \text{ mc} - F_x}{1.4 \text{ mc}}$$

T = Approximate number of required turns.

134.00 mc = Oscillator crystal frequency with slug of capacitor C46 set to the full counterclockwise position.

$F_x$  = Required local oscillator crystal frequency in megacycles.

1.4 mc = Frequency change for one turn of the slug of capacitor C46.

Example: Compute the approximate number of clockwise turns required to set the slug of capacitor C46 to a crystal frequency of 115.00 mc:

$$T = \frac{134.00 - 115.00}{1.4} = 13.5$$

NOTE

In subsequent step d, do not turn the slug of capacitor C46 more than 27 turns in a clockwise direction from the full counterclockwise position, or false tuning indications may be obtained.

d. Adjust the slug of oscillator capacitor C46 in a clockwise direction the number of turns computed in preceding step c.

5-35. OSCILLATOR ALIGNMENT.

a. Refer to figure 5-8, and observe the socket for crystal Y1 through the access hole in rear of chassis. If a crystal is installed in the socket, proceed as follows: Cover the jaws of a pair of long-nose pliers with plastic tape, grasp crystal Y1 with the pliers, and pull the crystal from the socket to the point where the crystal is stopped against the crystal mounting clip. Twist the crystal approximately 45 degrees to free it from the mounting clip, and withdraw the crystal from the access hole.

b. Fabricate a dummy crystal as shown in figure 5-10.

c. Use the long-nose pliers, and insert the dummy crystal into the socket for crystal Y1.

d. Adjust the signal generator for a 5-millivolt output

signal, modulated 30 percent at 1000 cps.

e. Set the AUDIO GAIN control on the front panel of the receiver to position 4. The POWER and CARRIER indicator lamps on the front panel should be illuminated.

f. Refer to figure 5-8, and adjust capacitor C46 *slowly* until the 1000-cps audio signal is heard in the headset.

g. If the 1000-cps audio signal in preceding step *f* cannot be discerned with a 5-millivolt output signal, adjust the slug of capacitor C46 to the full counterclockwise position, and increase the output of the signal generator to 50 millivolts. Turn the slug of capacitor C46 to the setting computed in step *c* of preceding paragraph 5-34, and *carefully* readjust the slug to the point where the 1000-cps signal just becomes audible.

h. Remove the dummy crystal installed in preceding step *c*.

i. A crystal of the frequency computed in step *b* of preceding paragraph 5-34 must be installed in the socket for crystal Y1. Grasp the crystal with the long-nose pliers so that the flat sides of the crystal are parallel with the bottom of the receiver chassis and the crystal pins are facing the access hole in rear of chassis. Insert the crystal through the access hole *above* the mounting clip, and start the crystal pins into the socket. Press the crystal down into the clip, and continue pressing the crystal forward until the pins are securely engaged in the socket.

j. *Carefully* adjust the frequency of the signal generator until a signal is heard in the headset.

k. Remove crystal Y1 as described in preceding step *a*, and install the dummy crystal in the crystal socket.

### NOTE

Before performing subsequent step *m*, make sure that the top and bottom equipment covers are secured in place on the receiver chassis.

l. If a signal is heard in the headset, proceed to subsequent step *m*. If no signal is heard, adjust capacitor C46 *slowly* until the signal is audible.

m. Remove the dummy crystal, and install crystal Y1 as described in preceding step *i*. The signal should be audible in the headset.

### 5-36. RF ALIGNMENT.

a. Set the AUDIO GAIN control on the front panel of the receiver to position 1.

b. Set the SQUELCH switch on the front panel of the receiver to the ON/MUTING position. Set the signal generator for 30 percent modulation at 1000 cps, and increase the output attenuator level of the signal generator *slowly* until the CARRIER indicator lamp on the front panel of the receiver is illuminated.

c. Adjust the AUDIO GAIN control on the front panel of the receiver for an indication of 1 watt on the power output meter.

d. Set the SQUELCH switch to the OFF position, and

reduce the output attenuator level of the signal generator until an indication of approximately 0.4 watt is obtained.

e. Refer to figure 5-8, and adjust capacitors C2, C4, C11, and C16 *in sequence* for a *peak* indication on the power output meter, reducing the output of the signal generator as necessary to maintain the indication *below* 0.6 watt.

f. Set the RF GAIN control on the front panel of the receiver to position 2, and adjust the output attenuator of the signal generator for an indication of approximately 0.6 watt on the power output meter.

g. Readjust capacitors C2, C4, C11, and C16 *in sequence* for a peak indication on the power output meter, reducing the output of the signal generator as necessary to maintain the indication below 0.6 watt.

### 5-37. MIXER AND I-F ALIGNMENT.

### NOTE

Mixer coil L6 and i-f transformers T1 and T2, figure 5-8, have been carefully adjusted at the factory for optimum performance. Readjustment is seldom required. If readjustment becomes necessary for reasons such as replacement of these parts or other associated parts, follow the procedure outlined in subsequent steps *a* through *i*.

a. Refer to figure 5-8, remove the eight mounting screws from the top equipment cover, and remove the cover.

b. Refer to figure 5-8, and detune capacitor C16 by turning the slug of the capacitor approximately three turns in a counterclockwise direction. This reduces the effects of receiver front-end noise, permitting accurate adjustment of the mixer and i-f circuits.

c. Set the AUDIO GAIN control on the front panel of the receiver to position 1.

d. Set the SQUELCH switch on the front panel of the receiver to the ON/MUTING position. Set the signal generator for 30 percent modulation at 1000 cps, and increase the output attenuator level of the signal generator *slowly* until the CARRIER indicator lamp on the front panel of the receiver is illuminated.

e. Set the AUDIO GAIN control on the receiver for an indication of 1 watt on the power output meter.

f. Set the SQUELCH switch to the OFF position, and reduce the output attenuator level of the signal generator until an indication of approximately 0.6 watt is obtained.

g. Refer to figure 5-8, and adjust the slug of coil L6 and/or transformers T1 and T2, as necessary, for a *peak* indication on the power output meter. Reduce the output of the signal generator during the tuning process to maintain the output indication *below* 0.6 watt.

h. Refer to figure 5-8, and readjust capacitor C16 for a peak indication on the power output meter, reducing the output of the signal generator as necessary to maintain the indication below 0.6 watt.

i. Reduce the output of the signal generator to zero, and

set the receiver SQUELCH switch to the ON/MUTING position.

j. Increase the output of the signal generator *slowly* until the CARRIER indicator lamp on the front panel of the receiver is illuminated. The output of the signal generator should be 3 microvolts or less.

k. Replace the top equipment cover on the receiver, and secure the cover in place with the eight mounting screws.

l. Return the receiver SQUELCH switch to the OFF position.

#### 5-38. ADJUSTMENT OF MUTING CONTROL.

##### NOTE

Muting control R36, figure 5-8, is included in the receiver to provide a means of establishing the audio output level when the receiver is operating below the squelch threshold. Effective range of the control is from -6 to -30 db, with maximum signal attenuation occurring in the full counterclockwise position. To adjust control R36, follow the procedure outlined in subsequent steps *a* through *i*.

a. Refer to figure 5-8, remove the eight mounting screws from the top equipment cover, and remove the cover.

b. Set the RF GAIN control on the front panel of the receiver to position 1.

c. Set the AUDIO GAIN control on the receiver to position 4.

d. Set the SQUELCH switch on the receiver to the ON/MUTING position.

e. Refer to figure 5-8, and adjust muting control R36 to the full clockwise position.

f. Set the signal generator for 30 percent modulation at 1000 cps, and adjust the output of the signal generator until the CARRIER indicator lamp on the receiver is illuminated.

g. Decrease the output of the signal generator until the CARRIER indicator lamp is extinguished.

h. Note the receiver audio output indicated on the power output meter, and adjust control R36 in a counterclockwise direction until the desired output reduction is achieved.

i. Replace the top equipment cover on the receiver, and secure the cover in place with the eight mounting screws.

#### 5-39. FINAL PROCEDURE.

a. Set the AUDIO GAIN control on the receiver to the PWR OFF position.

b. Disconnect all test equipment from the receiver.





## SECTION II

### SPECIAL MAINTENANCE

#### 5-40. GENERAL.

5-41. Should Special Maintenance be required, the pro-

cedures of Section I shall apply.

5-42. A schematic diagram of the receiver is shown in Figure 5-1 and a block diagram in Figure 5-2.

#### 5-43. SCHEMATIC DIAGRAMS

5-44. The following paragraphs are applicable to the schematic diagrams included in this section.

#### 5-45. REVISIONS

5-46. Page 5-46 is a schematic diagram of the receiver in which the major electrical changes will be made. It is a schematic diagram of the receiver in which the major electrical changes will be made. It is a schematic diagram of the receiver in which the major electrical changes will be made.

#### 5-47. SCHEMATIC DIAGRAMS

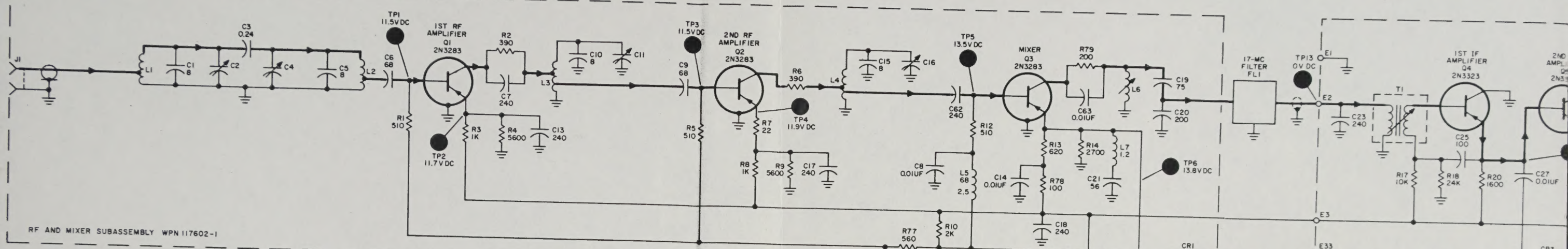
5-48. Schematic changes, such as a change in component values or minor wiring changes, will be illustrated and described on a temporary revision page. Temporary revision pages will be printed on yellow paper and will contain instructions for their use in the manual. The temporary pages shall be incorporated in the manual and retained until the original schematic is revised and reissued.

5-49. A complex change is a schematic or wiring diagram change which requires the removal of the original schematic in which the current change and all previous changes are included. Temporary revision pages that reflect changes not made by wiring and component changes shall be retained in the manual to show differences between the original equipment and equipment currently in production. Temporary revision pages shall be removed from the manual when a new schematic has been prepared and issued. The page or pages of change will be designated by a revision index number placed within a square box adjacent to the changed area on the schematic. This number will also appear in the schematic revision highlights area.

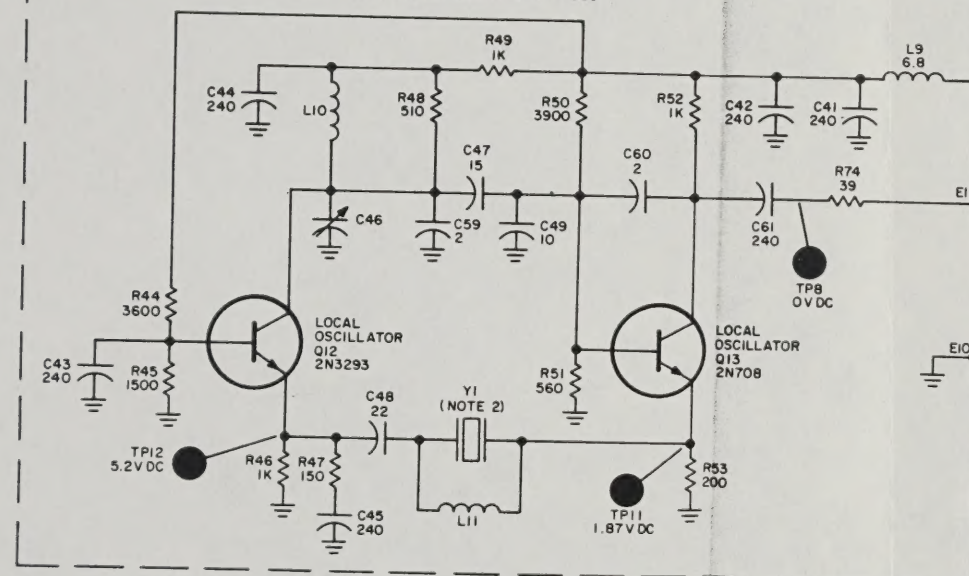
#### 5-51. REVISION HIGHLIGHTS

5-52. Schematic changes will be indicated by a revision highlight on which the reason for the change, and the nature of the change, are described. Each revision highlight will be numbered consecutively and will give the serial number of the unit in which the change was first made by the manufacturer.

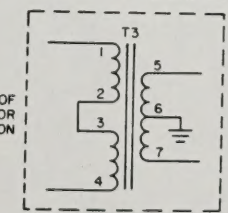
RF AND MIXER SUBASSEMBLY WPN 117602-1



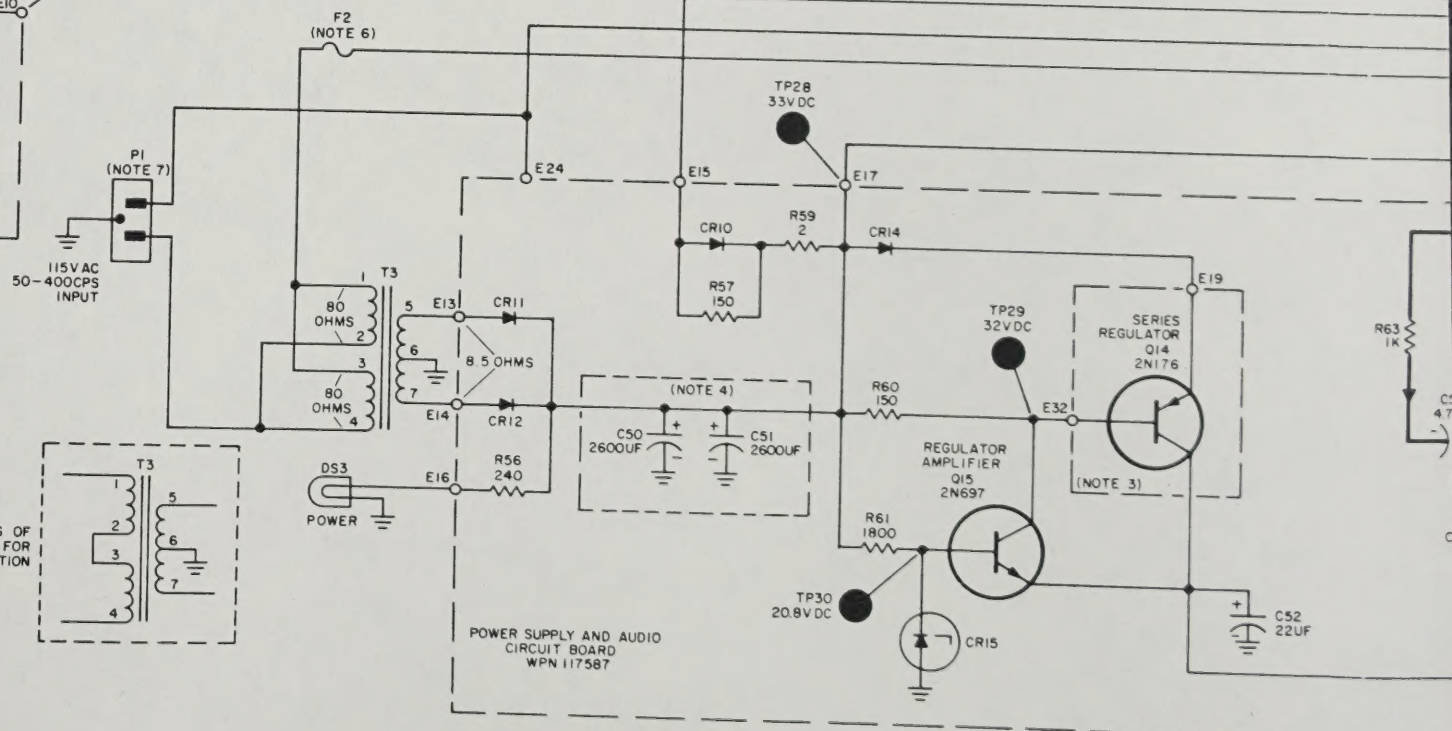
OSCILLATOR CIRCUIT BOARD WPN 117580



ALTERNATE WIRING OF TRANSFORMER T3 FOR 230-VOLT OPERATION



POWER SUPPLY AND AUDIO CIRCUIT BOARD WPN 117587



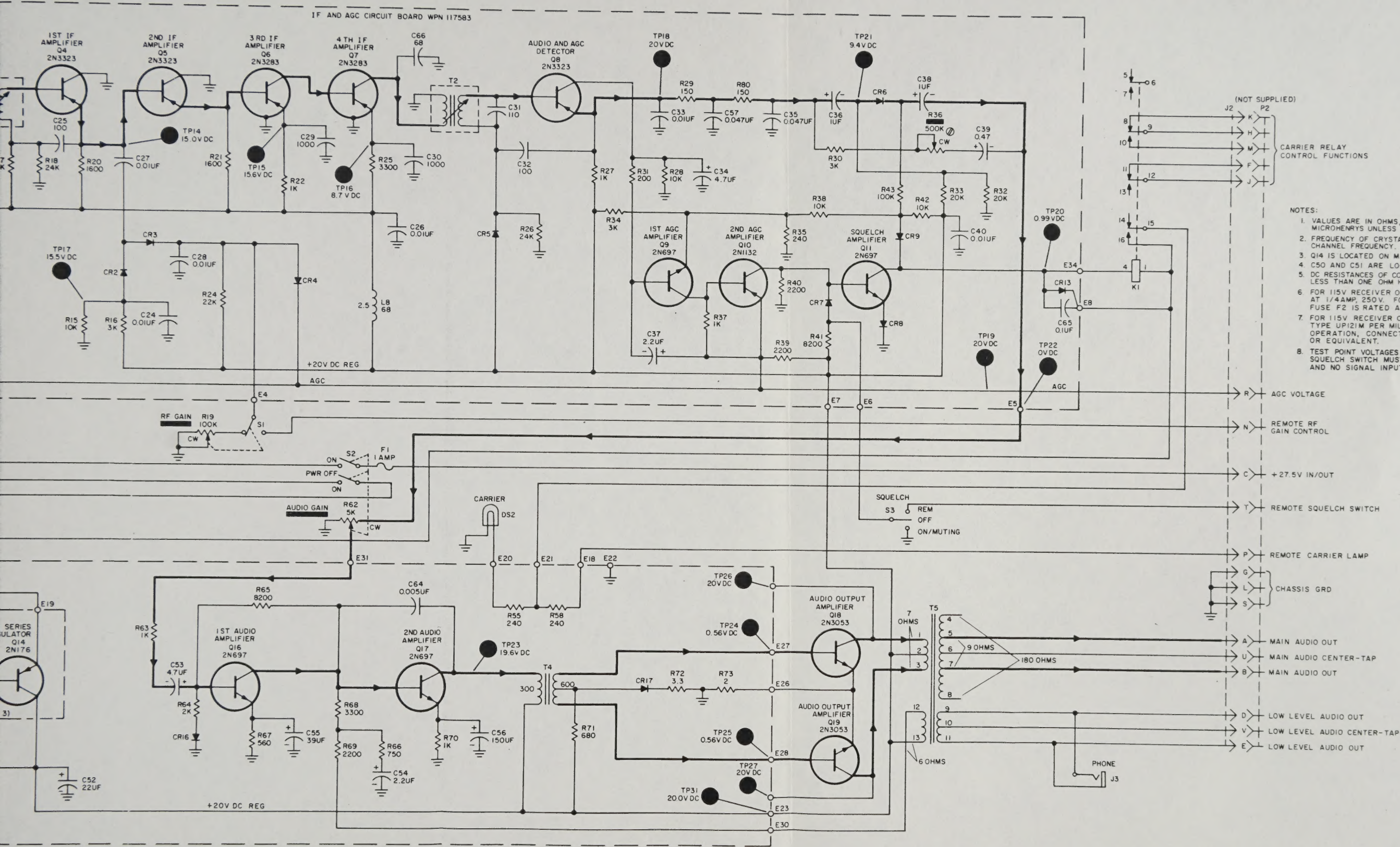


Figure 6-2. VHF Receiver Model 792A, Schematic Diagram

## SECTION II

### SPECIAL MAINTENANCE

#### 5-40. GENERAL.

5-41. Should Special Maintenance be required, the pro-

cedures of Section I shall apply.

5-42. A schematic diagram of the receiver is shown in Figure 5-1 and a block diagram in Figure 5-2.

#### 5-43. SCHEMATIC REVISION SERVICE

5-44. The following revision procedures are applicable to the schematic diagrams included in this section.

#### 5-45. REVISION AUTHORITY CHECK

5-46. Page 5-3A is a schematic revision authorization card which the VHF Receiver Company will maintain a record of all changes made to the circuit and will be updated and reissued, incorporating all necessary wiring pages, each time the schematic is revised.

#### 5-47. INFORMATION BULLET CHANGES

5-48. Simple changes, such as a change in component values or minor wiring changes, will be illustrated and described on a temporary revision page. Temporary revision pages will be printed on yellow paper and will contain instructions for their location in the manual. The temporary pages should be accumulated in the manual and retained with the original schematic in revised and reissued.

#### 5-49. INFORMATION, BULLET CHANGES

5-50. A complex change to a schematic, or an addition, deletion or simple changes, will require the issuance of a revised, permanent in which the current change and all previous changes are included. Temporary revision pages that reflect changes and make revisions necessary permanent should be retained in the manual. Allow differences between the original equipment and equipment currently in production. Temporary revision pages should be removed from the manual and kept in a separate file and schematics revision register. The point or points of change will be described by a Revision Information Sheet placed within a separate envelope to the changed sets on the equipment. This number will also appear in the schematic revision register.

#### 5-51. SERVICE BULLETINS

5-52. Schematic changes will be incorporated in service bulletins in which the reason for the change, and the nature of the change, are described. Each service bulletin will be numbered consecutively and will show the serial number of the last in which the change was made by the manufacturer.



## CHAPTER 6

### CIRCUIT DIAGRAMS

#### 6-1. GENERAL

6-2. A schematic diagram of the receiver is shown in figure 6-2 and a block diagram in figure 6-1.

#### 6-3. SCHEMATIC REVISION SERVICE

6-4. The following revision procedures are applicable to the schematic diagrams included in this section.

#### 6-5. REVISION REFERENCE PAGE

6-6. Page 6-3A is a schematic revision highlights table on which the Wilcox Electric Company will maintain a record of all changes made in the control unit. The table will be updated and re-issued, incorporating all temporary revision pages, each time the schematic is re-issued.

#### 6-7. SCHEMATICS, SIMPLE CHANGES

6-8. Simple changes, such as a change in component values or minor wiring changes, will be illustrated and described on a temporary revision page. Temporary revision pages will be printed on yellow paper and will contain instructions for their location in the manual. The temporary pages should be accumulated in the manual and retained until the original schematic is revised and re-issued.

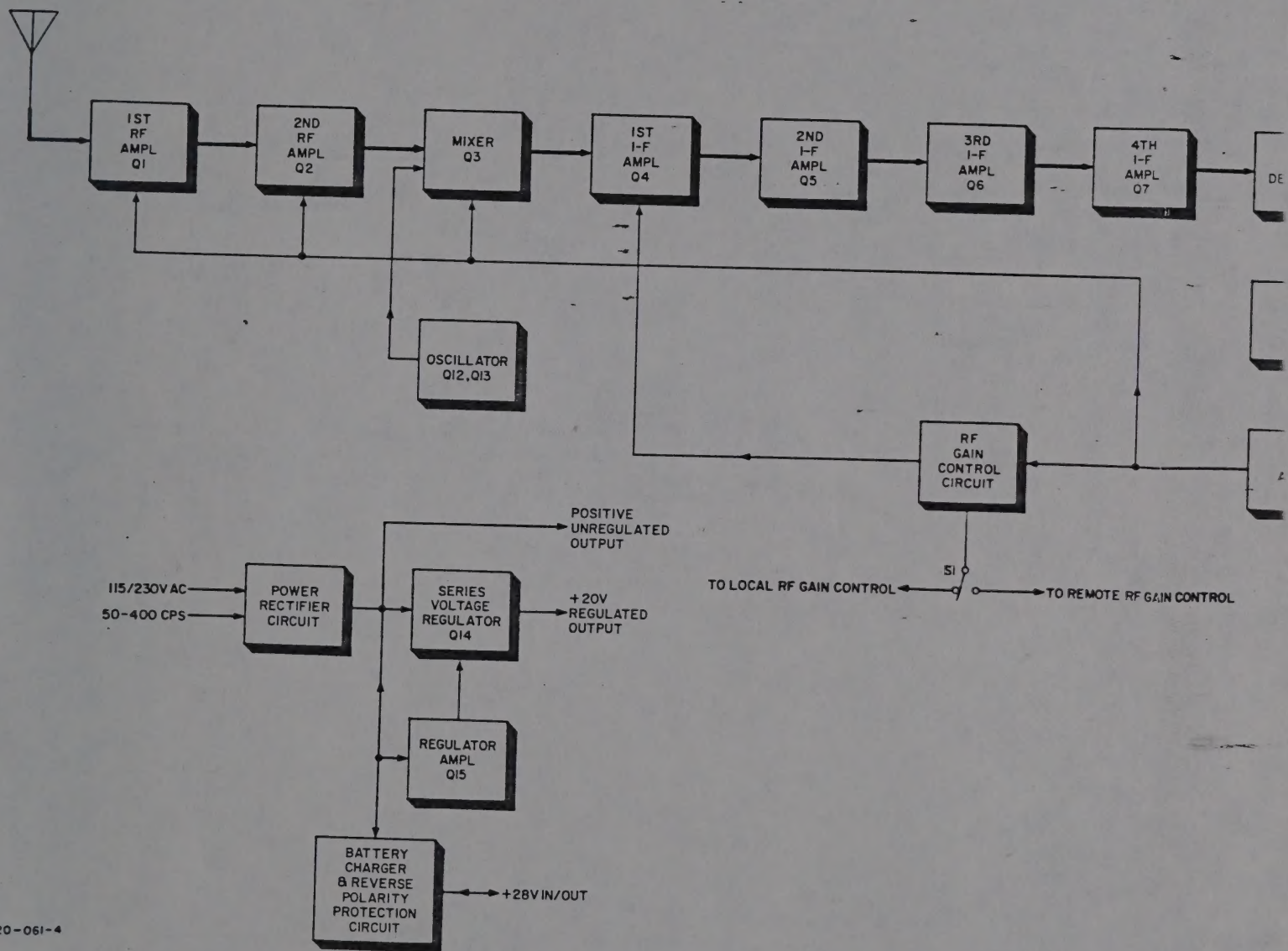
#### 6-9. SCHEMATICS, COMPLEX CHANGES

6-10. A complex change in a schematic, or an accumulation of simple changes, will require the issue of a new, revised, schematic in which the current change and all previous changes are included. Temporary revision pages that reflect changes not made by airline maintenance personnel should be retained in the manual to show differences between the airline's equipment and equipment currently in production. Temporary revision pages should be removed from the manual upon receipt of a new schematic and schematic revision highlights table. The point or points of change will be designated by a Revision Index Number placed within a square box adjacent to the changed area on the schematic. This number will also appear in the schematic revision highlights table.

#### 6-11. SERVICE BULLETINS

6-12. Schematic changes will be introduced by a service bulletin in which the reason for the change, and the nature of the change, are described. Each service bulletin will be numbered (consecutively) and will show the serial number of the unit in which the change was first made by the manufacturer.





120-061-4

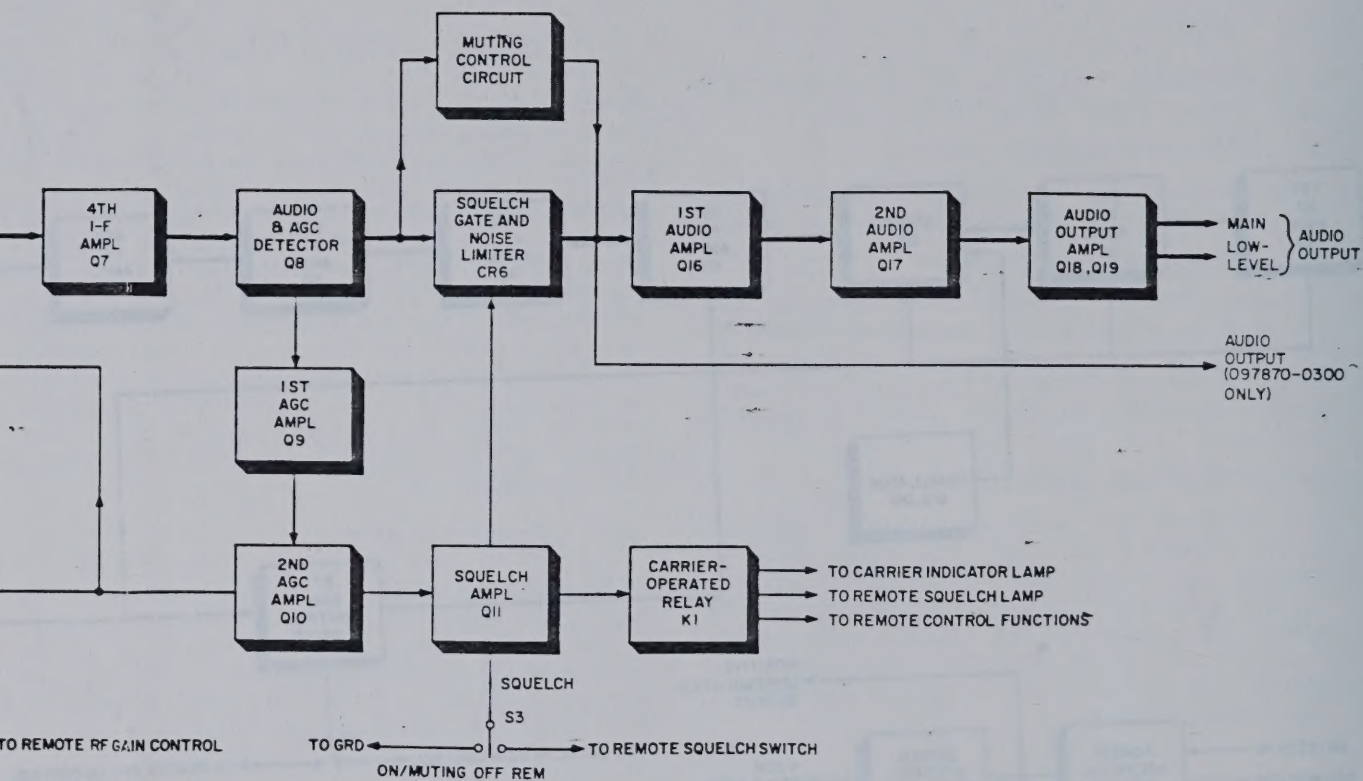


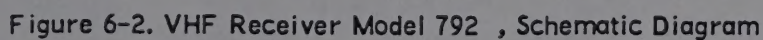
Figure 6-1. VHF Receiver Model 792 , Block Diagram

## VHF RECEIVER 792

## Schematic Revision Highlights

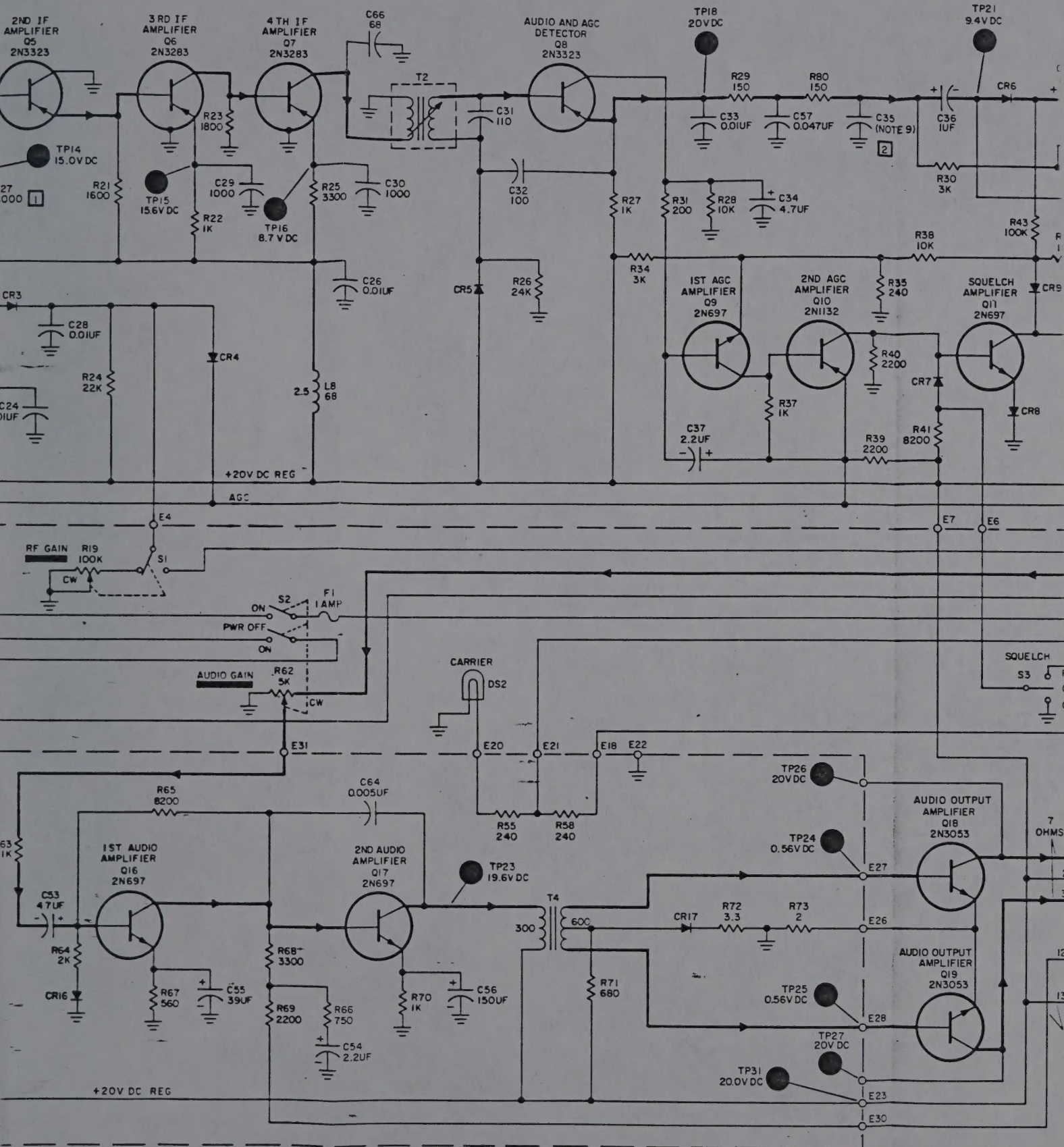
| FIG.<br>NO. | REVISION<br>INDEX<br>NUMBER | DESCRIPTION OF REVISION                                                                                                                                                                                                                                                           | SERIAL<br>NUMBER<br>EFFECTIVITY |
|-------------|-----------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------|
|             | 1                           | Added insert for Type 097870-0200 configuration. Added notes 9 and 10. Changed value of C1, C5, C10, and C15 from 8 uuf to NOTE 9. Change C27 from 0.01 uf to 3000 uuf.                                                                                                           | All                             |
|             | 2                           | Change value of C35 from 0.047 uf to NOTE 9. Added notes 11 and 12. Change insert to cover part Nos. 097870-0200 and -0300 configuration. Added connection from E5 to J2-L for part No. 097870-0300 only. Changed address of J2-L for part Nos. 097870-0100 and 097870-0200 only. | All                             |



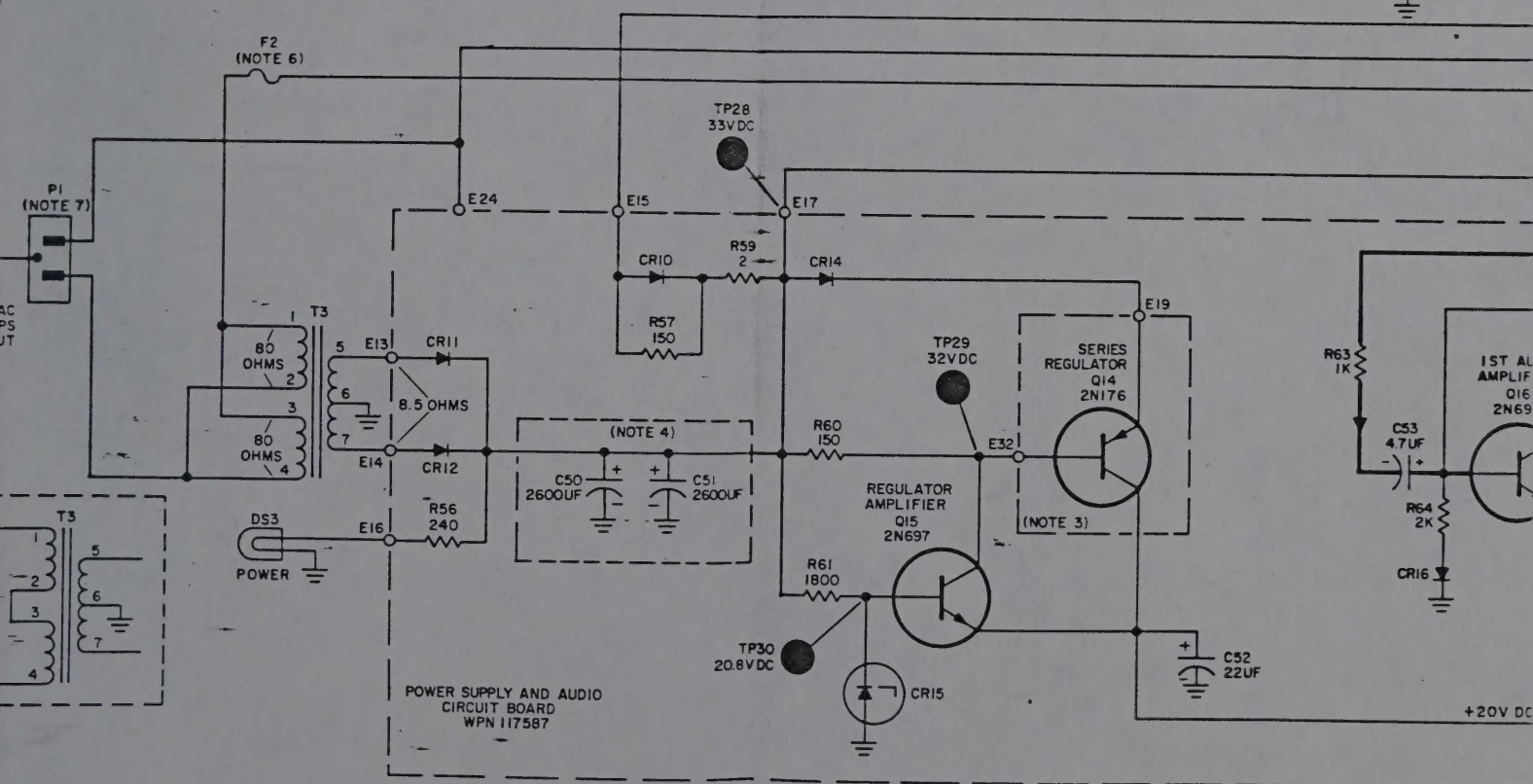
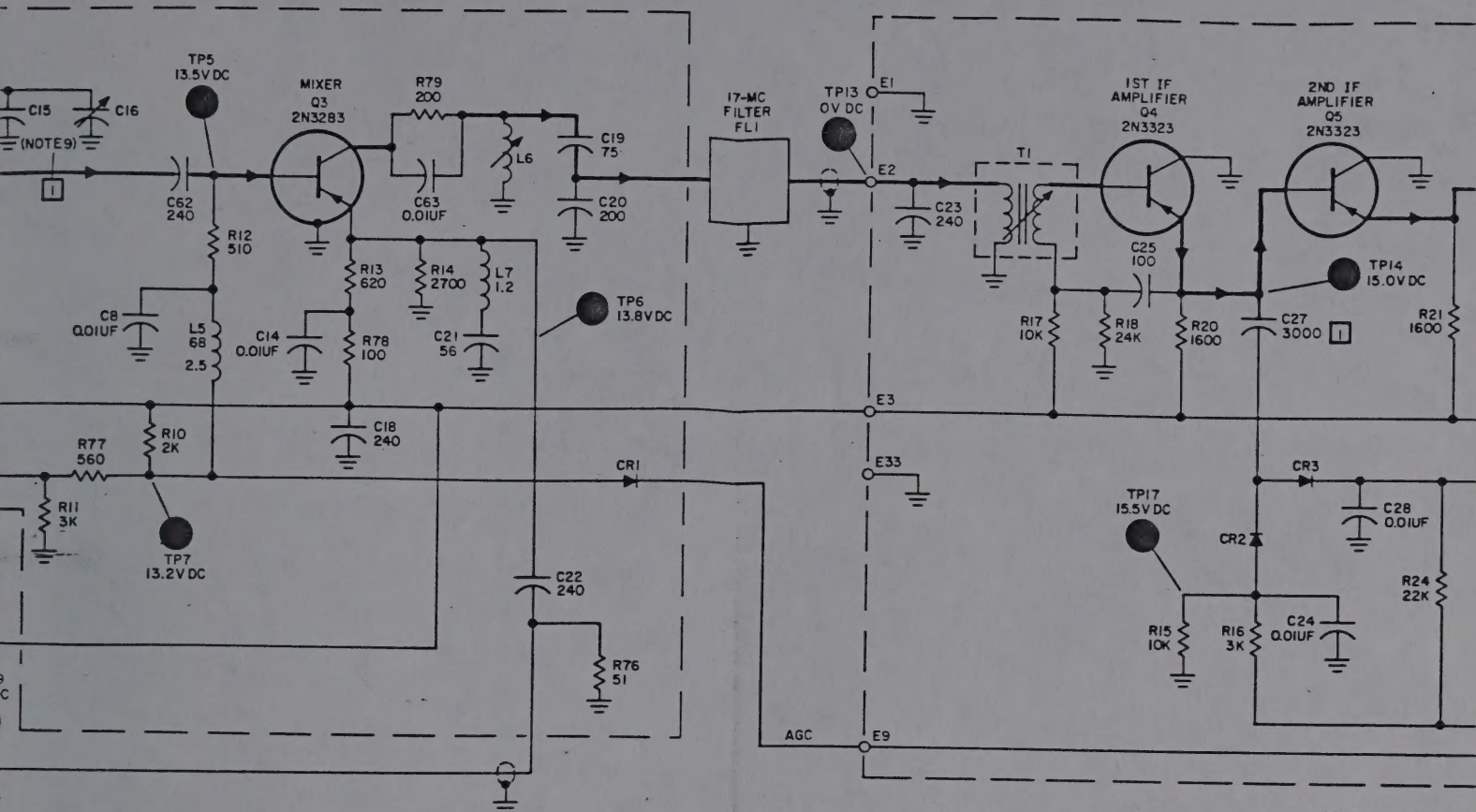




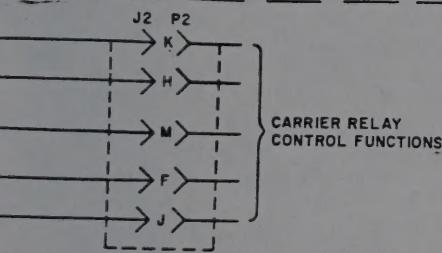
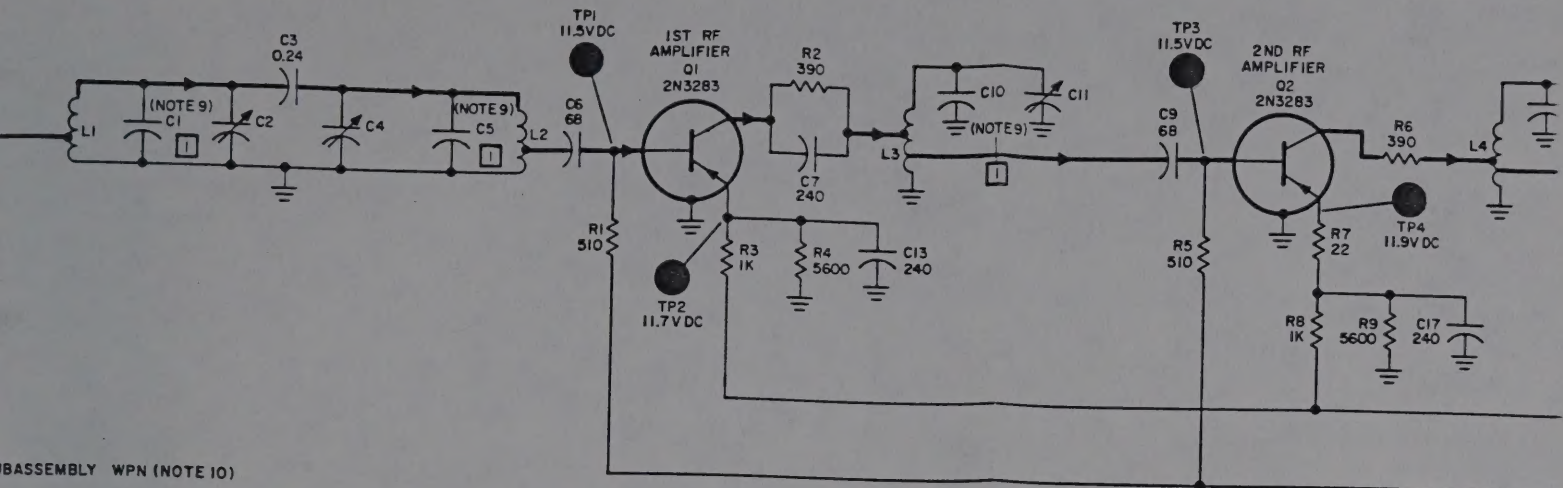
IF AND AGC CIRCUIT BOARD WPN 117583





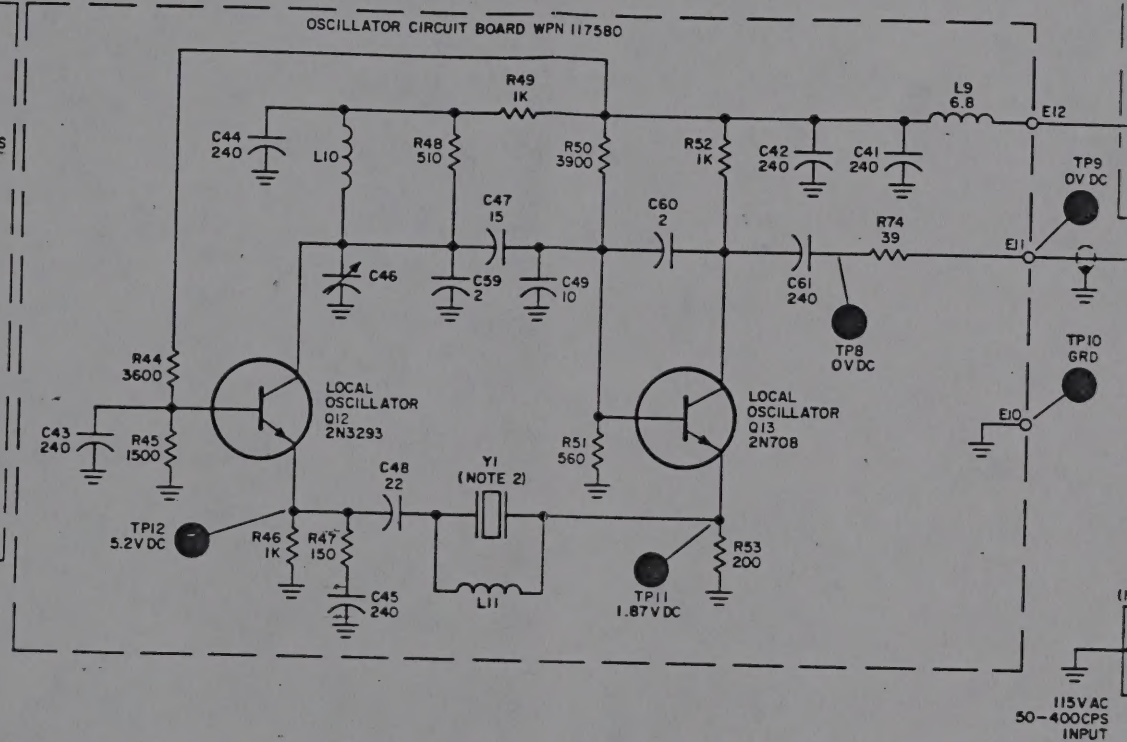




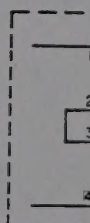


TO E23 (+20V DC REG)

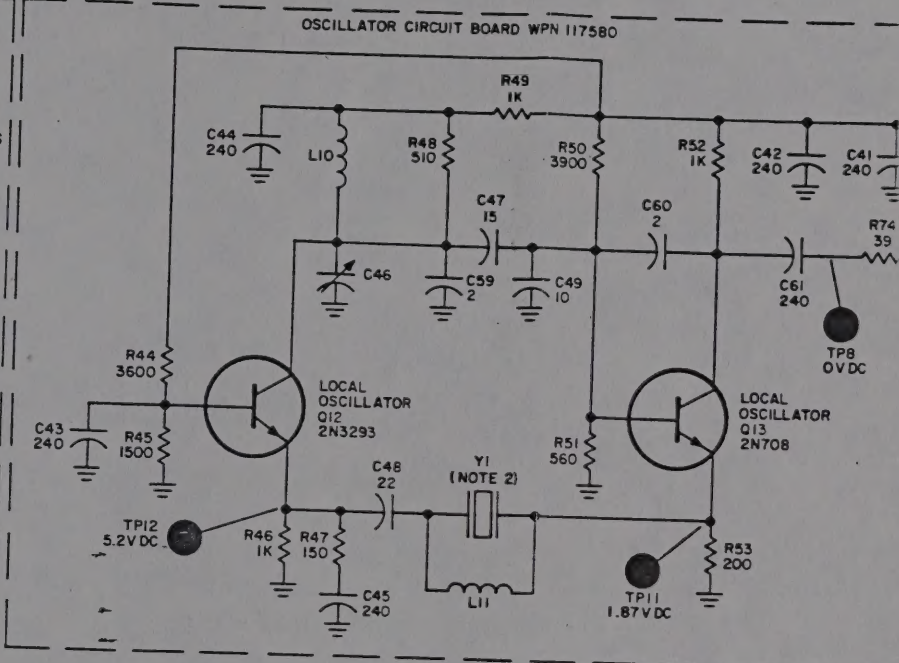
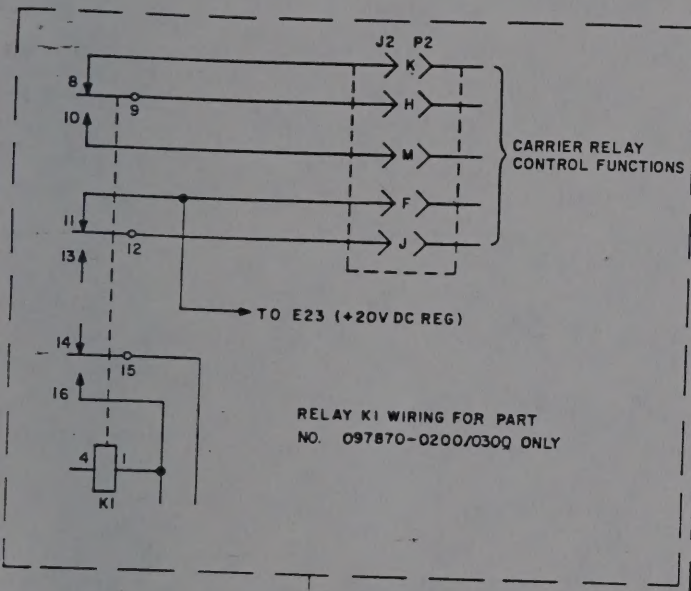
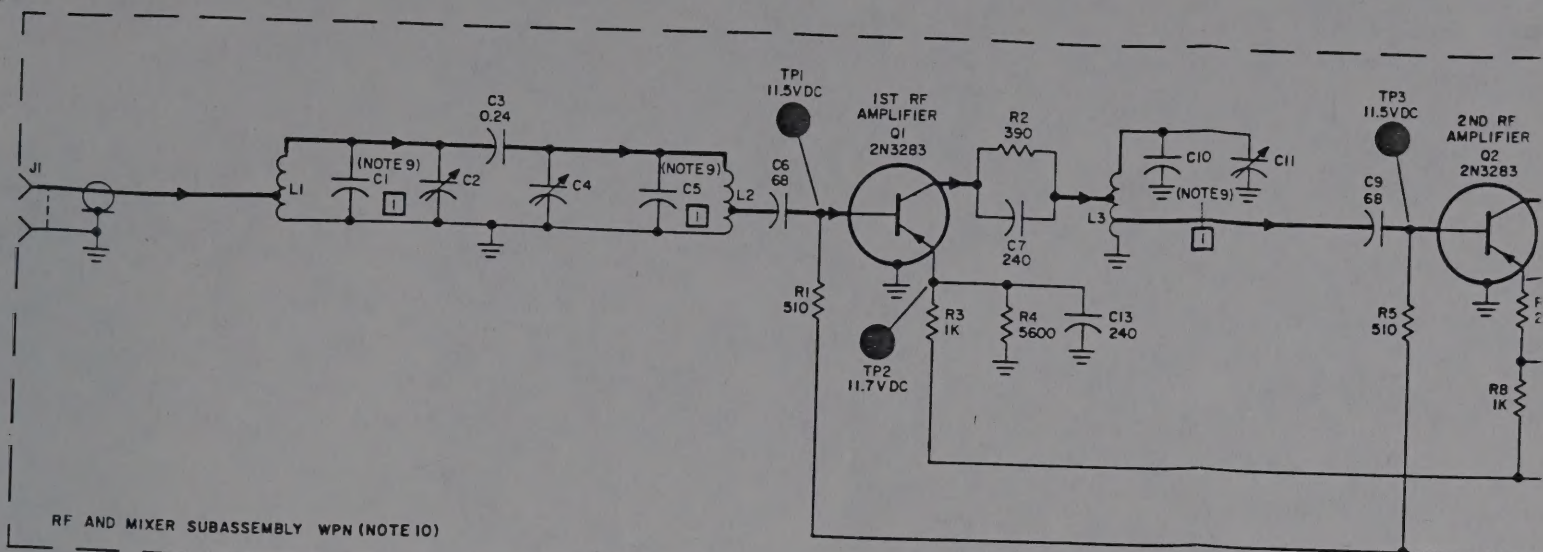
RELAY K1 WIRING FOR PART NO. 097870-0200/0300 ONLY



ALTERNATE WIRING OF TRANSFORMER T3 FOR 230-VOLT OPERATION







ALTERN  
TRANS  
230-



**VHF RECEIVER 792**

**CHAPTER 7**

**PARTS LIST**

**PART NO. 097870-0100/0200/0300**



# VHF RECEIVER 792

| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                                                                  | WILCOX<br>PART NO. |
|---------------------|-------------|----------------------------------------------------------------------------------------------|--------------------|
| R C1                |             | CAPACITOR, FXD, MICA: $8 \pm 0.5$ pf, 100 vdc (Part No. 097870-0100 - and -0300 only)        | 017456-0001        |
| C1                  |             | CAPACITOR, FXD, MICA: $15 \text{ pf} \pm 5\%$ , 100 vdc (Part No. 097870-0200 only)          | 017306-0001        |
| R C2                |             | CAPACITOR, VAR: 0.7 to 9 pf, 750 vdc                                                         | 015248-0001        |
| R C3                |             | CAPACITOR, FXD, COMP: $24 \text{ pf} \pm 5\%$ , 500 vdc                                      | 023290-0006        |
| R C4                |             | CAPACITOR, VAR: 0.7 to 9 pf, 750 vdc                                                         | 015248-0001        |
| R C5                |             | CAPACITOR, FXD, MICA: $8 \pm 0.5$ pf, 100 vdc (Part No. 097870-0100 and -0300 only)          | 017456-0001        |
| C5                  |             | CAPACITOR, FXD, MICA: $15 \text{ pf} \pm 5\%$ , 100 vdc (Part No. 097870-0200 only)          | 017306-0001        |
| R C6                |             | CAPACITOR, FXD, MICA: $68 \text{ pf} \pm 5\%$ , 100 vdc                                      | 017362-0002        |
| C7                  |             | CAPACITOR, FXD, MICA: $240 \text{ pf} \pm 5\%$ , 100 vdc                                     | 017414-0002        |
| R C8                |             | CAPACITOR, FXD, PLSTC: $0.01 \text{ uf} \pm 20\%$ , 80 vdc                                   | 019407-0001        |
| R C9                |             | CAPACITOR, FXD, MICA: $68 \text{ pf} \pm 5\%$ , 100 vdc                                      | 017362-0002        |
| R C10               |             | CAPACITOR, FXD, MICA: $8 \pm 0.5$ pf, 100 vdc (Part No. 097870-0100 and -0300 only)          | 017456-0001        |
| C10                 |             | CAPACITOR, FXD, MICA: $15 \text{ pf} \pm 5\%$ , 100 vdc (Part No. 097870-0200 only)          | 017306-0001        |
| R C11               |             | CAPACITOR, VAR: 0.7 to 9 pf, 750 vdc                                                         | 015248-0001        |
| C12                 |             | Not assigned                                                                                 |                    |
| C13                 |             | CAPACITOR, FXD, MICA: $240 \text{ pf} \pm 5\%$ , 100 vdc                                     | 017414-0002        |
| R C14               |             | CAPACITOR, FXD, PLSTC: $0.01 \text{ uf} \pm 20\%$ , 80 vdc                                   | 019407-0001        |
| R C15               |             | CAPACITOR, FXD, MICA: $8 \text{ pf} \pm 5\%$ , 100 vdc (Part No. 097870-0100 and -0300 only) | 017456-0001        |
| R C15               |             | CAPACITOR, FXD, MICA: $15 \pm 0.5$ pf, 100 vdc (Part No. 097870-0200 only)                   | 017306-0001        |
| R C16               |             | CAPACITOR, VAR: 0.7 to 9 pf, 750 vdc                                                         | 015248-0001        |
| C17                 |             | CAPACITOR, FXD, MICA: $240 \text{ pf} \pm 5\%$ , 100 vdc                                     | 017414-0002        |
| C18                 |             | CAPACITOR, FXD, MICA: $240 \text{ pf} \pm 5\%$ , 100 vdc                                     | 017414-0002        |
| C19                 |             | CAPACITOR, FXD, MICA: $75 \text{ pf} \pm 10\%$ , 100 vdc                                     | 017529-0001        |
| C20                 |             | CAPACITOR, FXD, MICA: $200 \text{ pf} \pm 10\%$ , 100 vdc                                    | 017410-0001        |
| C21                 |             | CAPACITOR, FXD, MICA: $56 \text{ pf} \pm 5\%$ , 100 vdc                                      | 017339-0001        |

# VHF RECEIVER 792

| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                                                    | WILCOX<br>PART NO. |
|---------------------|-------------|--------------------------------------------------------------------------------|--------------------|
| C22                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C23                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C24                 |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc                             | 019407-0001        |
| C25                 |             | CAPACITOR, FXD, MICA: 100 pf $\pm 5\%$ , 100 vdc                               | 017430-0002        |
| C26                 |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc                             | 019407-0001        |
| R C27               |             | CAPACITOR, FXD, MICA: 3000 pf $\pm 5\%$ , 100 vdc                              | 017319-0001        |
| C28                 |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc                             | 019407-0001        |
| C29                 |             | CAPACITOR, FXD, MICA: 1000 pf $\pm 5\%$ , 100 vdc                              | 017419-0002        |
| C30                 |             | CAPACITOR, FXD, MICA: 1000 pf $\pm 5\%$ , 100 vdc                              | 017419-0002        |
| C31                 |             | CAPACITOR, FXD, MICA: 110 pf $\pm 5\%$ , 100 vdc                               | 017354-0002        |
| C32                 |             | CAPACITOR, FXD, MICA: 100 pf $\pm 5\%$ , 100 vdc                               | 017430-0002        |
| C33                 |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc                             | 019407-0001        |
| C34                 |             | CAPACITOR, FXD, ELECT.: 4.7 uf $\pm 10\%$ , 35 vdc                             | 021207-0001        |
| C35                 |             | CAPACITOR, FXD, PLSTC: 0.047 uf $\pm 20\%$ , 80 vdc                            | 019407-0005        |
| R C35               |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc (Part No. 097870-0300 only) | 019407-0001        |
| C36                 |             | CAPACITOR, FXD, ELECT.: 1 uf $\pm 20\%$ , 35 vdc                               | 021183-0002        |
| R C37               |             | CAPACITOR, FXD, ELECT.: 2.2 uf $\pm 20\%$ , 35 vdc                             | 021161-0001        |
| C38                 |             | CAPACITOR, FXD, ELECT.: 1 uf $\pm 20\%$ , 35 vdc                               | 021183-0002        |
| C39                 |             | CAPACITOR, FXD, ELECT.: 0.47 uf $\pm 20\%$ , 35 vdc                            | 021201-0002        |
| C40                 |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm 20\%$ , 80 vdc                             | 019407-0001        |
| C41                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C42                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C43                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C44                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C45                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm 5\%$ , 100 vdc                               | 017414-0002        |
| C46                 |             | CAPACITOR, VAR: 0.8 to 18 pf, 750 vdc                                          | 015273-0001        |
| C47                 |             | CAPACITOR, FXD, CER: 15 pf $\pm 5\%$ , 200 vdc                                 | 023297-0001        |
| C48                 |             | CAPACITOR, FXD, MICA: 22 pf $\pm 5\%$ , 100 vdc                                | 017423-0002        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                        | WILCOX<br>PART NO. |
|---------------------|-------------|----------------------------------------------------|--------------------|
| C49                 |             | CAPACITOR, FXD, CER: 10 $\pm$ 0.5 pf, 200 vdc      | 023298-0001        |
| C50                 |             | CAPACITOR, FXD, ELECT.: 2600 uf +75%, -10%, 50 vdc | 021292-0001        |
| C51                 |             | CAPACITOR, FXD, ELECT.: 2600 uf +75%, -10%, 50 vdc | 021292-0001        |
| R C52               |             | CAPACITOR, FXD, ELECT.: 22 uf $\pm$ 20%, 35 vdc    | 021257-0001        |
| C53                 |             | CAPACITOR, FXD, ELECT.: 4.7 uf $\pm$ 20%, 10 vdc   | 021256-0001        |
| R C54               |             | CAPACITOR, FXD, ELECT.: 2.2 uf $\pm$ 20%, 35 vdc   | 021161-0001        |
| C55                 |             | CAPACITOR, FXD, ELECT.: 39 uf $\pm$ 20%, 10 vdc    | 021287-0001        |
| R C56               |             | CAPACITOR, FXD, ELECT.: 150 uf $\pm$ 10%, 15 vdc   | 021239-0001        |
| C57                 |             | CAPACITOR, FXD, PLSTC: 0.047 uf $\pm$ 20%, 80 vdc  | 019407-0005        |
| R C58               |             | Not assigned                                       |                    |
| R C59               |             | CAPACITOR, FXD, MICA: 2 $\pm$ 0.5 pf, 100 vdc      | 017408-0001        |
| R C60               |             | CAPACITOR, FXD, MICA: 2 $\pm$ 0.5 pf, 100 vdc      | 017408-0001        |
| C61                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm$ 5%, 100 vdc     | 017414-0002        |
| C62                 |             | CAPACITOR, FXD, MICA: 240 pf $\pm$ 5%, 100 vdc     | 017414-0002        |
| R C63               |             | CAPACITOR, FXD, PLSTC: 0.01 uf $\pm$ 20%, 80 vdc   | 019407-0001        |
| C64                 |             | CAPACITOR, FXD, CER: 0.005 uf +80%, -20%, 50 vdc   | 023261-0001        |
| R C65               |             | CAPACITOR, FXD, PLSTC: 0.1 uf $\pm$ 20%, 50 vdc    | 019279-0001        |
| C66                 |             | CAPACITOR, FXD, MICA: 68 pf $\pm$ 10%, 100 vdc     | 017362-0001        |
| CR1                 |             | SEMICONDUCTOR DEVICE, DIO: Ge, type 1N270          | 046122-0001        |
| CR2                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N4384         | 046220-0001        |
| CR3                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N4384         | 046220-0001        |
| CR4                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N627          | 046104-0001        |
| R CR5               |             | SEMICONDUCTOR DEVICE, DIO: Ge, type 1N270          | 046122-0001        |
| CR6                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N676          | 046204-0001        |
| CR7                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N627          | 046104-0001        |
| CR8                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N676          | 046204-0001        |
| CR9                 |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N627          | 046104-0001        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                                                      | WILCOX<br>PART NO. |
|---------------------|-------------|----------------------------------------------------------------------------------|--------------------|
| CR10                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N4384                                       | 046220-0001        |
| CR11                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N1082                                       | 246009-0001        |
| CR12                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N1082                                       | 246009-0001        |
| CR13                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N627                                        | 046104-0001        |
| CR14                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N4384                                       | 046220-0001        |
| CR15                |             | SEMICONDUCTOR DEVICE, DIO: Si, Zener, type 1N968B                                | 046171-0003        |
| CR16                |             | SEMICONDUCTOR DEVICE, DIO: Si, type 1N462                                        | 246008-0001        |
| CR17                |             | SEMICONDUCTOR DEVICE, DIO: Ge, type 1N270                                        | 046122-0001        |
| R DS1               |             | Not assigned                                                                     |                    |
| DS2                 |             | LAMP, INCAN: single cont midget flanged base, 28 v, 40 ma                        | 035627-0000        |
| DS3                 |             | LAMP, INCAN: single cont midget flanged base, 28 v, 40 ma                        | 035627-0000        |
| F1                  |             | FUSE, CTG: 1 amp, 250 v, time lag                                                | 814018-0002        |
| F2                  |             | FUSE, CTG: 1/8 amp, 250 v, time lag (for 230 v operation)                        | 814008-0002        |
| F2                  |             | FUSE, CTG: 1/4 amp, 250 v, time lag (for 115 v operation)                        | 814010-0002        |
| R FL1               |             | FILTER, BP: unbalanced; 500 ohms input, 200 ohms output; center frequency 17 MHz | 038188-0001        |
| J1                  |             | CONNECTOR, RECP, ELEC: rf type                                                   | 229673-0001        |
| J2                  |             | CONNECTOR, RECP, ELEC: 19 round male cont                                        | 030452-0000        |
| J3                  |             | JACK, TEL: single closed circuit, 2 conductor                                    | 029969-0000        |
| K1                  |             | RELAY, ELEC: 5 pole single throw; coil 1550 ohms, 28 vdc                         | 012198-0001        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                                       | WILCOX<br>PART NO. |
|---------------------|-------------|-------------------------------------------------------------------|--------------------|
| L1                  |             | COIL, RF: 5 turns No. 16 AWG wire                                 | 039029-0001        |
| L2                  |             | COIL, RF: 5 turns No. 16 AWG wire                                 | 039029-0001        |
| L3                  |             | COIL, RF: 5 turns No. 16 AWG wire                                 | 039029-0001        |
| L4                  |             | COIL, RF: 5 turns No. 16 AWG wire                                 | 039029-0001        |
| R L5                |             | COIL, RF: 68 uh $\pm 10\%$ at 2.5 MHz                             | 038952-0003        |
| L6                  |             | COIL, VAR: 1 uh $\pm 20\%$ at 25 MHz                              | 039030-0001        |
| L7                  |             | COIL, RF: 1.2 uh $\pm 10\%$ at 7.9 MHz                            | 038952-0018        |
| R L8                |             | COIL, RF: 68 uh $\pm 10\%$ at 2.5 MHz                             | 038952-0003        |
| L9                  |             | COIL, RF: 6.8 uh $\pm 10\%$ at 7.9 MHz                            | 038952-0005        |
| L10                 |             | COIL, RF: 5 turns No. 18 AWG wire                                 | 039026-0001        |
| L11                 |             | COIL, RF: 15 turns No. 24 AWG wire                                | 039027-0001        |
| P1                  |             | CONNECTOR, PLUG, ELEC: ac type, 2 flat male and 1 round male cont | 228595-0001        |
| P2                  |             | CONNECTOR, PLUG, ELEC: 19 round female cont                       | 029443-0000        |
| P3                  |             | CONNECTOR, PLUG, ELEC: rf type                                    | 875501-0002        |
| Q1                  |             | TRANSISTOR: PNP, Ge, type 2N3283                                  | 048633-0001        |
| Q2                  |             | TRANSISTOR: PNP, Ge, type 2N3283                                  | 048633-0001        |
| Q3                  |             | TRANSISTOR: PNP, Ge, type 2N3283                                  | 048633-0001        |
| Q4                  |             | TRANSISTOR: PNP, Ge, type 2N3323                                  | 048634-0001        |
| Q5                  |             | TRANSISTOR: PNP, Ge, type 2N3323                                  | 048634-0001        |
| Q6                  |             | TRANSISTOR: PNP, Ge, type 2N3283                                  | 048633-0001        |
| Q7                  |             | TRANSISTOR: PNP, Ge, type 2N3283                                  | 048633-0001        |
| Q8                  |             | TRANSISTOR: PNP, Ge, type 2N3323                                  | 048634-0001        |
| R Q9                |             | TRANSISTOR: NPN, Si, type 2N697                                   | 048513-0001        |
| R Q10               |             | TRANSISTOR: PNP, Si, type 2N1132                                  | 048565-0001        |
| Q11                 |             | TRANSISTOR: NPN, Si, type 2N697                                   | 048513-0001        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                        | WILCOX<br>PART NO. |
|---------------------|-------------|----------------------------------------------------|--------------------|
| Q12                 |             | TRANSISTOR: NPN, Si, type 2N3293                   | 048642-0001        |
| Q13                 |             | TRANSISTOR: NPN, Si, type 2N708                    | 048610-0001        |
| Q14                 |             | TRANSISTOR: PNP, Ge, type 2N176                    | 048641-0001        |
| Q15                 |             | TRANSISTOR: NPN, Si, type 2N697                    | 048513-0001        |
| Q16                 |             | TRANSISTOR: NPN, Si, type 2N697                    | 048513-0001        |
| Q17                 |             | TRANSISTOR: NPN, Si, type 2N697                    | 048513-0001        |
| Q18                 |             | TRANSISTOR: NPN, Si, type 2N3053                   | 048630-0001        |
| Q19                 |             | TRANSISTOR: NPN, Si, type 2N3053                   | 048630-0001        |
| R1                  |             | RESISTOR, FXD, COMP: 510 ohms $\pm 5\%$ , 1/4 w    | 849550-0001        |
| R2                  |             | RESISTOR, FXD, COMP: 390 ohms $\pm 5\%$ , 1/4 w    | 849545-0001        |
| R3                  |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w   | 849560-0001        |
| R4                  |             | RESISTOR, FXD, COMP: 5600 ohms $\pm 5\%$ , 1/4 w   | 849587-0001        |
| R5                  |             | RESISTOR, FXD, COMP: 510 ohms $\pm 5\%$ , 1/4 w    | 849550-0001        |
| R6                  |             | RESISTOR, FXD, COMP: 390 ohms $\pm 5\%$ , 1/4 w    | 849545-0001        |
| R7                  |             | RESISTOR, FXD, COMP: 22 ohms $\pm 5\%$ , 1/4 w     | 849732-0001        |
| R8                  |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w   | 849560-0001        |
| R9                  |             | RESISTOR, FXD, COMP: 5600 ohms $\pm 5\%$ , 1/4 w   | 849587-0001        |
| R10                 |             | RESISTOR, FXD, COMP: 2000 ohms $\pm 5\%$ , 1/4 w   | 849571-0001        |
| R11                 |             | RESISTOR, FXD, COMP: 3000 ohms $\pm 5\%$ , 1/4 w   | 849577-0001        |
| R12                 |             | RESISTOR, FXD, COMP: 510 ohms $\pm 5\%$ , 1/4 w    | 849550-0001        |
| R13                 |             | RESISTOR, FXD, COMP: 620 ohms $\pm 5\%$ , 1/4 w    | 849553-0001        |
| R14                 |             | RESISTOR, FXD, COMP: 2700 ohms $\pm 5\%$ , 1/4 w   | 849575-0001        |
| R15                 |             | RESISTOR, FXD, COMP: 10,000 ohms $\pm 5\%$ , 1/4 w | 849596-0001        |
| R16                 |             | RESISTOR, FXD, COMP: 3000 ohms $\pm 5\%$ , 1/4 w   | 849577-0001        |
| R17                 |             | RESISTOR, FXD, COMP: 10,000 ohms $\pm 5\%$ , 1/4 w | 849596-0001        |
| R18                 |             | RESISTOR, FXD, COMP: 24,000 ohms $\pm 5\%$ , 1/4 w | 849610-0001        |
| R19                 |             | RESISTOR, VAR, COMP: 100,000 ohms $\pm 10\%$ , 2 w | 054352-0001        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                          | WILCOX<br>PART NO. |
|---------------------|-------------|------------------------------------------------------|--------------------|
| R20                 |             | RESISTOR, FXD, COMP: 1600 ohms $\pm 5\%$ , 1/4 w     | 849568-0001        |
| R21                 |             | RESISTOR, FXD, COMP: 1600 ohms $\pm 5\%$ , 1/4 w     | 849568-0001        |
| R22                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w     | 849560-0001        |
| R23                 |             | RESISTOR, FXD, COMP: 1800 ohms $\pm 5\%$ , 1/4 w     | 849569-0001        |
| R24                 |             | RESISTOR, FXD, COMP: 22,000 ohms $\pm 5\%$ , 1/4 w   | 849608-0001        |
| R25                 |             | RESISTOR, FXD, COMP: 3300 ohms $\pm 5\%$ , 1/4 w     | 849578-0001        |
| R26                 |             | RESISTOR, FXD, COMP: 24,000 ohms $\pm 5\%$ , 1/4 w   | 849610-0001        |
| R27                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w     | 849560-0001        |
| R28                 |             | RESISTOR, FXD, COMP: 40,000 ohms $\pm 5\%$ , 1/4 w   | 849596-0001        |
| R29                 |             | RESISTOR, FXD, COMP: 150 ohms $\pm 5\%$ , 1/4 w      | 849530-0001        |
| R30                 |             | RESISTOR, FXD, COMP: 3000 ohms $\pm 5\%$ , 1/4 w     | 849577-0001        |
| R31                 |             | RESISTOR, FXD, COMP: 200 ohms $\pm 5\%$ , 1/4 w      | 849535-0001        |
| R32                 |             | RESISTOR, FXD, COMP: 20,000 ohms $\pm 5\%$ , 1/4 w   | 849607-0001        |
| R33                 |             | RESISTOR, FXD, COMP: 20,000 ohms $\pm 5\%$ , 1/4 w   | 849607-0001        |
| R34                 |             | RESISTOR, FXD, COMP: 3000 ohms $\pm 5\%$ , 1/4 w     | 849577-0001        |
| R35                 |             | RESISTOR, FXD, COMP: 240 ohms $\pm 5\%$ , 1/4 w      | 849538-0001        |
| R36                 |             | RESISTOR, VAR, COMP: 500,000 ohms $\pm 20\%$ , 1/4 w | 054295-0001        |
| R37                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w     | 849560-0001        |
| R38                 |             | RESISTOR, FXD, COMP: 10,000 ohms $\pm 5\%$ , 1/4 w   | 849596-0001        |
| R39                 |             | RESISTOR, FXD, COMP: 2200 ohms $\pm 5\%$ , 1/4 w     | 849572-0001        |
| R40                 |             | RESISTOR, FXD, COMP: 2200 ohms $\pm 5\%$ , 1/4 w     | 849572-0001        |
| R41                 |             | RESISTOR, FXD, COMP: 8200 ohms $\pm 5\%$ , 1/4 w     | 849593-0001        |
| R42                 |             | RESISTOR, FXD, COMP: 10,000 ohms $\pm 5\%$ , 1/4 w   | 849596-0001        |
| R43                 |             | RESISTOR, FXD, COMP: 100,000 ohms $\pm 5\%$ , 1/4 w  | 849632-0001        |
| R44                 |             | RESISTOR, FXD, COMP: 3600 ohms $\pm 5\%$ , 1/4 w     | 849580-0001        |
| R45                 |             | RESISTOR, FXD, COMP: 1500 ohms $\pm 5\%$ , 1/4 w     | 849566-0001        |
| R46                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w     | 849560-0001        |
| R47                 |             | RESISTOR, FXD, COMP: 150 ohms $\pm 5\%$ , 1/4 w      | 849530-0001        |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                      | WILCOX<br>PART NO. |
|---------------------|-------------|--------------------------------------------------|--------------------|
| R48                 |             | RESISTOR, FXD, COMP: 510 ohms $\pm 5\%$ , 1/4 w  | 849550-0001        |
| R49                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w | 849560-0001        |
| R50                 |             | RESISTOR, FXD, COMP: 3900 ohms $\pm 5\%$ , 1/4 w | 849581-0001        |
| R51                 |             | RESISTOR, FXD, COMP: 560 ohms $\pm 5\%$ , 1/4 w  | 849551-0001        |
| R52                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w | 849560-0001        |
| R53                 |             | RESISTOR, FXD, COMP: 200 ohms $\pm 5\%$ , 1/4 w  | 849535-0001        |
| R54                 |             | Not assigned                                     |                    |
| R55                 |             | RESISTOR, FXD, COMP: 240 ohms $\pm 5\%$ , 1 w    | 851175-0000        |
| R56                 |             | RESISTOR, FXD, COMP: 240 ohms $\pm 5\%$ , 1 w    | 851175-0000        |
| R57                 |             | RESISTOR, FXD, FILM: 150 ohms $\pm 5\%$ , 2 w    | 861635-0001        |
| R58                 |             | RESISTOR, FXD, COMP: 240 ohms $\pm 5\%$ , 1 w    | 851175-0000        |
| R59                 |             | RESISTOR, FXD, WW: 2 ohms $\pm 5\%$ , 3 w        | 050953-0001        |
| R60                 |             | RESISTOR, FXD, COMP: 150 ohms $\pm 5\%$ , 1/4 w  | 849530-0001        |
| R61                 |             | RESISTOR, FXD, COMP: 1800 ohms $\pm 5\%$ , 1/2 w | 850206-0000        |
| R62                 |             | RESISTOR, VAR, COMP: 5000 ohms $\pm 10\%$ , 1 w  | 054351-0001        |
| R63                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w | 849560-0001        |
| R64                 |             | RESISTOR, FXD, COMP: 2000 ohms $\pm 5\%$ , 1/4 w | 849571-0001        |
| R65                 |             | RESISTOR, FXD, COMP: 8200 ohms $\pm 5\%$ , 1/4 w | 849593-0001        |
| R66                 |             | RESISTOR, FXD, COMP: 750 ohms $\pm 5\%$ , 1/4 w  | 849556-0001        |
| R67                 |             | RESISTOR, FXD, COMP: 560 ohms $\pm 5\%$ , 1/4 w  | 849551-0001        |
| R68                 |             | RESISTOR, FXD, COMP: 3300 ohms $\pm 5\%$ , 1/4 w | 849578-0001        |
| R69                 |             | RESISTOR, FXD, COMP: 2200 ohms $\pm 5\%$ , 1/4 w | 849572-0001        |
| R70                 |             | RESISTOR, FXD, COMP: 1000 ohms $\pm 5\%$ , 1/4 w | 849560-0001        |
| R71                 |             | RESISTOR, FXD, COMP: 680 ohms $\pm 5\%$ , 1 w    | 851191-0000        |
| R72                 |             | RESISTOR, FXD, COMP: 3.3 ohms $\pm 5\%$ , 1/4 w  | 050961-0001        |
| R73                 |             | RESISTOR, FXD, WW: 2 ohms $\pm 5\%$ , 3 w        | 050953-0001        |
| R74                 |             | RESISTOR, FXD, COMP: 39 ohms $\pm 5\%$ , 1/4 w   | 849740-0001        |
| R75                 |             | Not assigned                                     |                    |

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| SYMBOL/<br>ITEM NO. | FIG.<br>NO. | DESCRIPTION                                                                                                                                                                                                   | WILCOX<br>PART NO. |
|---------------------|-------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| R76                 |             | RESISTOR, FXD, COMP: 51 ohms $\pm 5\%$ , 1/4 w                                                                                                                                                                | 849514-0001        |
| R77                 |             | RESISTOR, FXD, COMP: 560 ohms $\pm 5\%$ , 1/4 w                                                                                                                                                               | 849551-0001        |
| R78                 |             | RESISTOR, FXD, COMP: 100 ohms $\pm 5\%$ , 1/4 w                                                                                                                                                               | 849524-0001        |
| R79                 |             | RESISTOR, FXD, COMP: 200 ohms $\pm 5\%$ , 1/4 w                                                                                                                                                               | 849535-0001        |
| R80                 |             | RESISTOR, FXD, COMP: 150 ohms, $\pm 5\%$ , 1/4 w                                                                                                                                                              | 849530-0001        |
| S1                  |             | Integral part of R19                                                                                                                                                                                          |                    |
| S2                  |             | Integral part of R62                                                                                                                                                                                          |                    |
| S3                  |             | SWITCH, TGL: SP3T                                                                                                                                                                                             | 812079-0001        |
| T1                  |             | TRANSFORMER, RF: prim, 5-3/4 turns No. 24 magnet wire close wound over sec; sec 9-1/4 turns No. 24 magnet wire close wound; slug tuned prim, 0.20 uh, $\pm 20\%$ at 25 MHz; sec 0.40 uh, $\pm 20\%$ at 25 MHz | 042253-0001        |
| T2                  |             | TRANSFORMER, RF: prim, 5-1/4 turns No. 24 magnet wire close wound over sec; sec 9-1/4 turns No. 24 magnet wire close wound; slug tuned prim, 0.20 uh, $\pm 20\%$ at 25 MHz; sec 0.40 uh, $\pm 20\%$ at 25 MHz | 042254-0001        |
| R T3                |             | TRANSFORMER, PWR: prim, 115 or 230 v rms, 45 to 450 Hz, single phase; sec 53.5 v rms, 280 ma center tapped                                                                                                    | 040228-0001        |
| T4                  |             | TRANSFORMER, AF: prim, impedance 300 ohms, center tapped; sec impedance 600 ohms; frequency response 300 to 3750 Hz                                                                                           | 040211-0001        |
| T5                  |             | TRANSFORMER, AF: prim, 21 v rms, 1000 Hz, single phase, center tapped; sec 143 v rms, 25 v rms, 4.6 v rms, and 2.5 v rms                                                                                      | 040227-0001        |
| R Y1                |             | CRYSTAL UNIT, QTZ: frequency to be specified by customer                                                                                                                                                      |                    |



## SECTION IV

### PREPARATION FOR RESHIPMENT

2-29. INTRODUCTION. This section gives you instructions necessary to prepare the receiver for reshipment by any standard cargo carrier. The shipping case in which the receiver is packed for shipment is small and light enough for transport in light military vehicles.

#### 2-30. REMOVAL OF EQUIPMENT.

a. Turn the receiver off by setting the AUDIO GAIN control (see figure 2-4) to the PWR OFF position.

b. Remove the interconnecting cable, antenna cable, and headset from the receiver. Disconnect the ac power cable from the ac outlet.

c. Remove the four mounting screws from the front panel of the receiver, and remove the receiver from the equipment rack.

d. Make sure that the equipment covers on the top and bottom of the receiver chassis are securely mounted prior to packing the receiver.

#### 2-31. PACKING INSTRUCTIONS.

2-32. No special instructions are required to package the receiver for reshipment. Use standard military packaging procedures when packing the receiver in the shipping case. A desiccant dehydrator should be used in the shipping case.

